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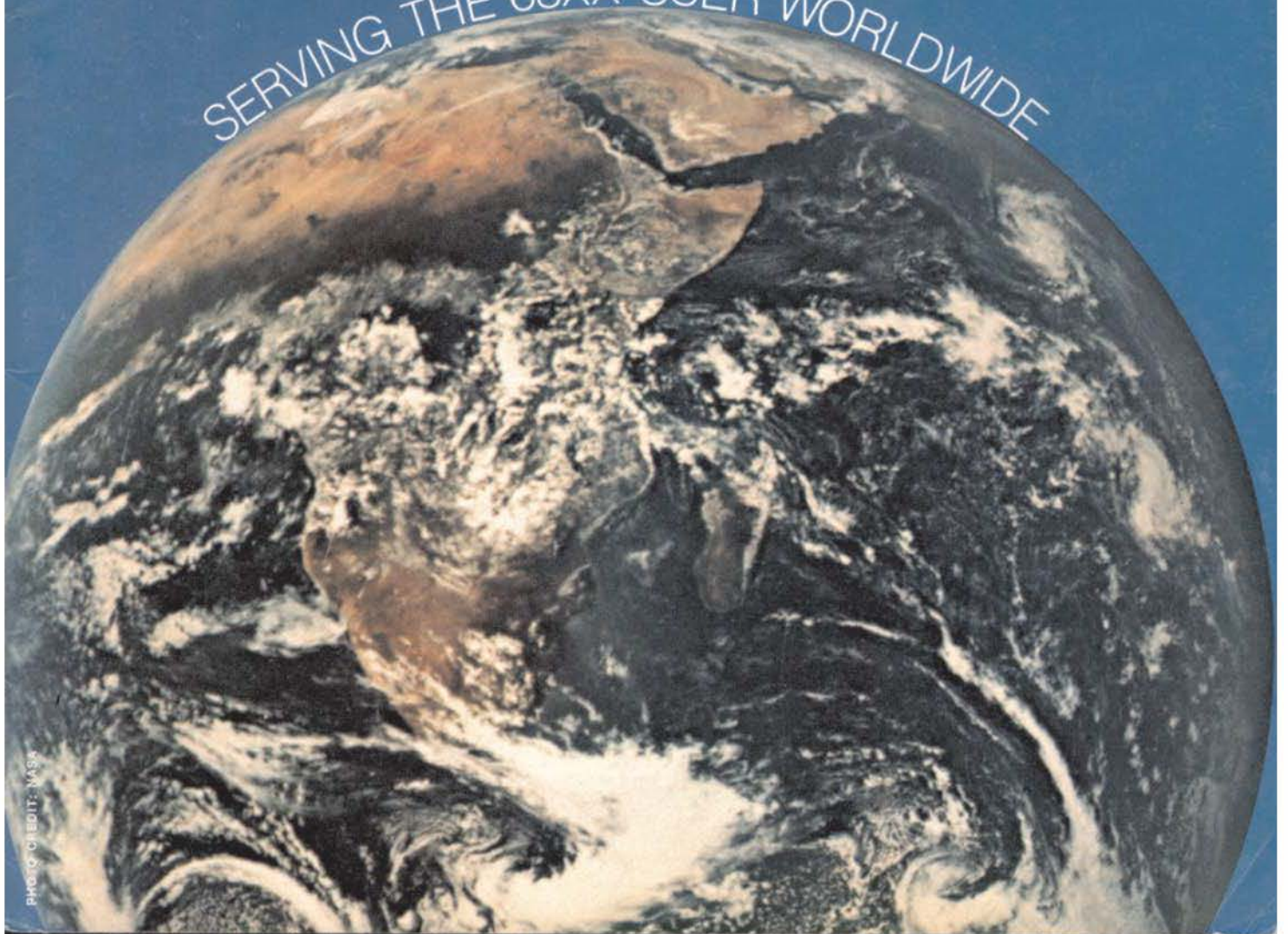
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# Flex User Notes

Ronald W. Anderson  
3340 Sturbridge Court  
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## SOME THOUGHTS

The name of this text file on my disk is NOTES40. To bring up some ancient history, I decided some time ago to start a FLEX User's Group newsletter. In the first year as an "Independent", I managed to get out 8 newsletters, and had some 60 or so, "members" or "subscribers". At that point, the writing was fun, but the pains of getting copies made and mailed was more than I had time for, so I contacted Don Williams to see if he would be interested in turning my newsletter into a column.

I remember thinking that I shouldn't promise to get a column out every month. Don looked at some of my early efforts, and offered to send my "subscribers" the first four issues of '68' Micro Journal in which my column would appear, as a fulfillment of my obligation, since I had indicated that the subscribers would receive 12 issues of a newsletter.

The title NOTES40 means just that. This is newsletter number 40. The first four were done with Miniflex, and I can steer anyone interested to a source for them, since a "Society for the Preservation and Encouragement of Miniflex Use in America" (I'm just kidding with the name, of course) has been established. I have the newsletters, but the software listings associated with them would be of little use to most of the readers of '68'. What I am getting at, is the question of whether more than three of you readers would be interested in a collection of about three years worth of these columns, of course complete with program listings as they have appeared from time to time.

If you might be interested, please send a quick note or card to '68' Micro Journal and express your interest. If Don Williams receives enough response, it is very likely that he will put all my efforts together and print them.

## WHY I LIKE PASCAL

In 1000 words or less.... Just kidding. My arm is a bit out of joint from patting myself on the back, but I will describe what I did and why briefly should anyone care. I've been working on a machine control program. It is about 12K of object code in OmegaSoft Pascal. Compile - Assemble - Load time total about 12 minutes. I had written the program to be easily modified for a range of machines in different configurations. I put a half page of constants at the beginning. (The listing is 15 pages). Now, for each machine, I only had to edit that first half page and recompile the program, burn a set of EPROMs, and the job is done.

Sounds reasonable, and it was not bad at all. Suddenly had a mild brainstorm. I have one EPROM on the processor board addressed at F800 to FFFF, so I can have restart and IRQ vectors defined. That just takes a few bytes at the very end of memory, leaving almost 2K empty. Why not declare those constants as INTEGER variables at absolute addresses starting at \$F800? Now the Assembler file I used to call START, can contain the machine parameters, entered by using FDB decimal numbers. The main Pascal program need not be edited and recompiled. I simply edit a two page Assembler program, assemble it (takes about 30 seconds) and burn one EPROM, and the machine is "personalized". If we goof, and the characteristics of the machine require a slight change in one or more of the constants, it becomes a 5 minute change rather than a half hour. (It takes a good ten minutes to burn 6 2716 EPROMs if you are all organized.)

The whole thing took about two hours to implement. A simple change of the Constant declarations to Variables, followed by an edit of the START program was all it took. The thing worked on the first try. A side benefit is that I don't have to keep a very large disk file for each machine, just a 5 sector configuration file. There is an added benefit. If there are some machine constants

that might improve the performance if "tweaked" a bit, I can modify them and run an experiment "instantly" compared to the previous method.

## CURED AT LAST

I have had a long standing and rather irritating problem with my SWTP system. I've had a pair of 8" drives for just two years now. When I first installed them, I noticed that I had no trouble formatting a disk if I did it immediately after turning the computer on. After half an hour, I noticed a large increase in head "re-seeks" when trying to read a disk. After a bit of experimenting, I found that I had to cool something on the DMAF board. I had been running my SWTP box with the cover off, since I change boards and connections rather frequently. I didn't have a fan handy, so I made a "stovepipe" out of a piece of cardboard and found that setting it on top of the cards, roughly over the DMAF, made the system settle down.

Not wanting to have the only computer in town with a chimney, I installed a fan in the case and glued paper inside to close up most of the holes in the ventilated top. I put the fan at the right side of the front of the box, just in front of the power supply, and left an opening at the left side so that air could be brought in there and drawn across the cards. The problem went away... for a while. It came back a year later, and got so bad, even with the fan, that I couldn't format a disk. I carefully marked the position of the three "trimpots" in the data separator circuit, and broke the seals. I found that rotating one of them slightly made the system read fine again. I think now, that that particular problem was unrelated to the long term one.

At any rate, the problem recently returned. I found, with some circuit cooling spray that cooling the 1791 disk controller made the system work again. My temporary cure was to mount the fan in reverse, outside the case and let it blow directly on the 1791. I plugged the DMAF card into the board slot where it got the most air from the fan. Since then I've replaced the 1791 and had no problem again operating with the cover removed. I agree, it doesn't look as nice, but it sure facilitates transfer of a couple of 5" drives back and forth between it and the Color Computer.

I have heard from one other FLEX user with similar problems. Perhaps some of the 1791's are just a bit marginal in some specification that is critical to the operation in the DMAF, and a few extra degrees temperature rise causes them to stop working properly. I'd be interested to hear if anyone else has had similar problems. I've been operating the system all evening with the top off, and had no problems at all.

## LONG CORD

My computer is located in the basement, next to the furnace. I get a bit tired of listening to the blower kick on and off every half hour when the weather starts to get cold again. Further, I long for a change of scenery once in a while. Sometimes I am working on a program that requires 30 seconds of operator input followed by 15 minutes of compile or assemble time, a five minute test, quick edit, and a recompile. For all those reasons, I decided that it would be nice to put my terminal on an extension cord. The idea only works if I am doing a lot of editing, writing, or testing that involves only my system disk and one other. That is, it is not terribly handy to be running down the basement steps every five minutes to change disks. Frequently, however, that condition is fulfilled, and I find that I can bring the terminal up to the family room and watch TV with the family between interactions with the terminal.

I have about 25 feet of plain ordinary multi conductor wire cord between my ADM-3A and the computer. I am running the terminal at 19,200 baud, and there has not been a "burp" all evening. I've edited this text to this point and typed in several pages of a manual I am working on. Recently, I spent half a day with the terminal on the redwood picnic table on the screened porch, enjoying the great late summer weather while working on a work project.

## AN APPLICATION

At least one reader has asked me to present some sort of application of a computer to the real world. A long time ago, I became interested by necessity, in the use of thermistors to measure temperature. A thermistor is made of a semiconductor material, and its primary characteristic is a resistance that changes with temperature, decreasing as the temperature increases. What is unique about the thermistor is not that its resistance changes with temperature, since all metals change in their resistance to electric current with temperature. Metals show a very small increase in resistance with a change in temperature. What is unique about a thermistor is the magnitude of its resistance change, and the fact that the resistance decreases as temperature increases. We would say that the thermistor has a large negative temperature coefficient of resistance. The coefficient is in the range of 2% to 4% per Celsius Degree.

The resistance of the thermistor may be measured by one of several means. For computer applications, it is desirable to measure a voltage with an A/D converter, rather than a current. If the thermistor is connected in series with a fixed resistor, across a voltage source, and the voltage across the fixed resistor measured, it will be found to change with temperature, since the thermistor and the resistor form what is called a voltage divider, and since one of the resistors changes value, the ratio of the voltage divider changes.

I once reasoned that there might be a "best value" for the series resistor that would yield a voltage output that is nearly linear over a specified temperature range. I wrote some equations to solve for the fixed resistor that would result in the output voltage from the divider being linear for three temperatures, the lowest, highest, and midpoint of the temperature range desired. It turns out that the equations are "solvable" easily, and that the resulting value does give a pretty good approximation to a linear output voltage with temperature. The maximum error depends on the temperature coefficient of the thermistor and the temperature range.

For a common thermistor produced by a manufacturer called FENWAL, (in particular, a bead thermistor type GA45P2), a temperature range of 15 C. to 35 C. (50 to 86 F), the maximum error is approximately 1/16 C. degree. The error curve is approximately sinusoidal, the departure from linear being positive for one half of the scale and negative for the other.

I recently became interested in the problem again, and wrote a BASIC program to calculate the best series resistor, expected voltage output, and maximum errors for a given thermistor. Thermistors have two parameters that must be known in order to perform the calculation. First is their nominal resistance at 25 degrees C. Second is their coefficient, variously expressed by different manufacturers. Fenwal uses a coefficient Beta such that the resistance at any temperature may be expressed (with R0 representing the resistance at 25 degrees C.) by the following equation:

$$R = R0 * \exp(Beta * (1/(T + 273) - 1/298))$$

The value T+273 is the temperature in degrees Kelvin (Absolute), and the 298 corresponds to 25 degrees C. in Kelvin. I'll include the program here. The voltages calculated are volts per volt applied to the thermistor resistor combination. A JPC A/D card may well be used to measure temperature with such a setup. First, the thermistor divider network output is run through an "operational amplifier" that subtracts the voltage at the minimum temperature so that the output is zero for that temperature. The amplifier has voltage gain so that the output at the high end of the temperature range is 5 volts. The temperature then, calculated from the integer value from the A/D converter for our example of 15 to 35 degrees C. (a range of 20 degrees) connected to the output of the amplifier, would be: A/D output \* 20/255 + 15. To express the temperature in degrees F.

you would use A/D out \* 36/255 + 50. Note that the maximum error in voltage from the thermistor is less than the resolution of the 8 bit A/D for the example given here.

Thermistors have large tolerances on their nominal resistances, and they might therefore be in error by several degrees without calibration. The resistor in series with the thermistor should be variable, and adjusted for correct reading at minimum temperature. A lab thermometer and a bucket of water will provide a good enough reference. Ice may be used to adjust the temperature to 50 degrees F, and the series resistor set for the correct output of zero volts from the amplifier. The bucket of water should then be brought to 86 degrees F, and the amplifier feedback resistor (gain) set to produce 5 volts out of the amplifier. The unit should now be calibrated to the accuracy of your thermometer with a maximum additional error of .062 degrees.

I don't need to say that the JPC card has 16 inputs, and that you may connect several thermistors and amplifiers to several inputs to monitor temperatures at various locations. The resistance (50K) of the thermistor at 25 degrees, is high enough so that long wires won't appreciably change the calibration. Suppose you put several of these around the house, and used a potentiometer connected from reference voltage to ground on another A/D input, as a temperature "command". You could have up to 8 "thermostats" one in each room of your house and some to spare. Now, connect your computer to control motors that open and shut baffles in your hot air ducts to the various rooms in response to the difference between the set and measured temperature, and you will have a zone heating system that will save you lots of fuel all winter.

Should you not want to tie up your computer all winter, the software for this system could easily be put in EPROM and run in a "single board" computer. Output signals to open and close baffles would depend on the type of actuator you can find. Generally, just writing an "open" or "close" signal to a parallel port, could be sufficient with a little hardware to interpret the logic level at the port. I will continue next month with further descriptions of software that could be used to do this project. Of course, you would have to bypass your normal thermostat connection so that if any zone were requesting heat, you would start the furnace and supply heat to that area. To keep the furnace from cycling too often you would probably want to build in a timer such that an area would have to demand heat for a few minutes before starting the furnace. The BASIC program that I wrote for linearizing the thermistor output calculates a series resistor of the value shown in the circuit diagram, for the GA45P2 probe thermistor from FENWALL. I will include the program listing here, so you can use other available thermistors if you have the data available.

```

10 REM THIS PROGRAM WILL CALCULATE THE VALUE OF SERIES
20 REM RESISTOR NECESSARY TO LINEARIZE THE OUTPUT OF A
30 REM THERMISTOR OVER A SPECIFIED TEMPERATURE RANGE.
40 REM THE THERMISTOR PARAMETERS WILL BE ASKED FOR.
50 REM THE MIN AND MAX TEMPERATURES CENTIGRADE WILL BE INPUT.
60 REM THE PROGRAM WILL CALCULATE THE BEST VALUE FOR THE SERIES
70 REM RESISTOR, AND THEN CALCULATE THE VOLTAGE OUTPUT OF THE
80 REM NETWORK FOR MIN AND MAX TEMPERATURE, AND THE VOLTS PER
90 REM DEGREE, ALL AS A FRACTION OF THE SUPPLY VOLTAGE.
100 REM
110 REM THE PROGRAM WILL THEN SEARCH THE INTERVAL FOR THE
120 REM MAXIMUM ERROR (BOTH HIGH AND LOW) AND REPORT THAT
130 REM IN DEGREES.
140 REM
150 DEF FNT(X)=EXP(B*(1/(X+273.15)-1./298.15))
160 REM
170 REM 298 IS ABSOLUTE (KELVIN) VALUE FOR 25 DEGREES C.
180 REM X IS TEMPERATURE IN CENT. DEGREES, R0 IS RES AT
190 REM 25 DEGREES C. B IS A CONSTANT FOR THE THERMISTOR.
200 REM

```





followed by the cursor. A FLEX Command consists of three things; the Filename (the name of the command, program, etc.), the filename Extension (CMD for Command, TXT for Text, DAT for Data, etc.), and the Drive Number (0 for the first Disk Drive, 1 for the second Disk Drive, etc.). Also, you can use EITHER a Space OR a Comma for Command separators or delimiters. (As we shall see, most times you only need to use the Filename, the System knows the rest of the information automatically.) For example, if you want to see a CATalog of the Disk in Drive #1, you COULD enter

O.CAT.CMD 1 — or — O.CAT.CMD,1

and the list of the Files on the Disk in Drive 1 would be displayed on the Screen. I said COULD because you would normally just enter

CAT

and FLEX would look for a File called "CAT.CMD" on Drive 0, and would know that you want that Command to operate on Drive 1 (because of the ASN.CMD, which we will be discussing next month). The Drive Number does not have to appear first in the Command Line; it can also follow the Extension. Both of the following Commands would be interpreted the same;

O.CAT.CMD 1 or CAT.CMD.O 1

I referred to the System Drive and Work Drive a little earlier. The "SYSTEM Drive" is the Drive containing the Disk with all (or at least MOST) of the COMMANDS on it; these will ALL have a ".CMD" Extension. When FLEX expects a Command (it expects you to tell it WHAT you want it to do first), it will automatically look for the Filename you give to have a ".CMD" Extension, AND it will automatically look on the specified SYSTEM Drive for it. When we just entered "CAT" above, it assumed that we were referring to CAT.CMD on the System Drive. Also, it assumed we were wanting it to do what we requested to the Disk in the WORK Drive. The WORK Drive contains the Disk we are normally "WORKing" on. The System and Work Drive CAN be one and the same, but it is much better to have at least two Disk Drives on your System when using FLEX. There are two reasons for this. First, you will rapidly fill up even a Double Sided Double Density Disk with COMMANDS. Most Utilities, such as COPY, DELETE, CAT, etc., are short - one to five Sectors - but a lot of the Programs are large. The EDITOR is forty some Sectors, the ASSEMBLER is forty some Sectors, STYLO is around 86 Sectors, and DYNACALC is over a hundred; not to mention some of the DICTIONARIES which run over 300 Sectors. Second, most of the Disk Writing is done to the Work Disk; if something goes wrong, files are destroyed by Writing all over them, not by Reading them, so the Disk being Written too is more susceptible to damage (it is normally NOT a problem with FLEX; I have only had a couple of problems over two years of CONSTANT use with FLEX, where I averaged a problem a week with some other Systems I have been using).

(By the way, the Color Disk System does NOT come up after Turn-On with the Disk Read and Write VERIFICATION turned ON, like FLEX does; you need to enter "VERIFY ON" each time you bring the Color Computer up with the Disk System installed.)

#### The DISK SYSTEM

Just what does the term "Disk System" refer too? The Disk System consists of at least ONE Disk Drive, the Disk Controller, the Computer, and a Disk Operating System (DOS) Program. The Disk Drives hold the Disk, rotate it at a very tightly controlled speed (300 RPM for 5 1/4" Disks), position the Read/Write Head per the Controllers Instructions, and either Reads information off of the Disk or Writes information to it. The Disk Controller is the communication link between the Computer and the Disk Drive. It executes the Computers instructions. If the Computer asks the Controller to get the information from a certain Track and Sector, the Controller tells the Drive to position the Read/Write Head at that Track Number, checks off the Sectors until the correct one shows up, grabs the information as it goes by, and tells the Computer what it just read. If the Computer instructs the Controller to Write a Sector to the Disk, the Controller tells the Drive to position the Read/Write Head at the proper Track, checks the Sectors as they go by, and causes the Drive to Write the information when the correct Sector shows up. The Disk OPERATING System (FLEX or the Radio Shack Disk System), keeps track of what is going on, determines which Track and Sector it wants to operate on, and just what information is to be written to the Disk. The DOS is the "brains" of the outfit; all of the others just follow its instructions.

But, all Computers and Disk Controllers are not alike. If they were, we would have COMPLETE compatibility between ALL Computer Systems that run FLEX, for instance. Since this is not the case, some variations occur, and each Computer System must contain routines which "marry" the DOS to the Computer and Disk Controller. Since FLEX was written to be a "general" Disk Operating System, TSC said "OK, put a character from the Keyboard HERE!", and did not worry about what kind of Keyboard was to be used. "Keyboard Drivers", short Software routines, must then be written for each Computer System that is to work with FLEX. The same goes for Displays and Disk Controllers. TSC sells a Program called "FLEX.COR" that is the "heart" of the FLEX DOS; each Computer System Manufacturer or FLEX Conversion Writer then develops Keyboard, Display, and Disk Drivers which are married to FLEX to make up the Program "FLEX.SYS" which is loaded into the Computer each time you want to run the FLEX Operating System. There are two other "System" routines that are normally found on a FLEX Disk. These are "PRINT.SYS", which marries the FLEX DOS to the Printer Output, and "ERRORS.SYS", which provides the User with an English Language translation of an error, such as "Drives Not Ready", instead of some weird message like "Error No. 4".

#### The Floppy DISK

Now let's look at the Floppy Disk itself, for a moment. 5 1/4" Floppy Disks come in several different flavors. You can get Single Sided, Single Density certified Disks; Single Sided Double Density certified Disks; the same versions for Double Sided; and ALL of these versions as Soft Sector, 10 Sector Hard Sector, 16 Sector Hard Sector, etc., etc., etc. Well, happily, we can narrow the choice down a "chunk". First, lets see what these different "certifications" mean.

When the Disk is Manufactured, a coating of magnetic material is deposited on the surface to hold the information we want to store on the it. If the Disk is being made for Single Density use, the coating does not need to be very high quality, because there is not going to be as much information stored on a certain amount of the Disks Surface. But, if it is designed for Double Density storage, there will be a lot more information stored on the same amount of surface. Therefore, the coating needs to be higher quality (and usually, thicker). Obviously, this coating costs more. The Disk Manufacturer tests the Disks for certain capabilities, and certifies them accordingly. Luckily for us Users, the Manufacturers have developed their procedures to the point that even though they will only certify the less expensive coating for Single Density, it actually works fine for Double Density use. The other major factor that makes this possible is the development of excellent Disk Controllers and Read/Write Heads in the Disk Drives. We at '68' Micro have been using GOOD QUALITY (Verbatim DataLife, Scotch 3M, etc.) Single Sided Double Density 5 1/4" Diskettes for Double Density Double Sided operation for several years with NO problems of any kind.

With the Disk Controller technology of today, the critical factor becomes the Read/Write Head in the Disk Drive. That little tiny piece of hardware will make or break your whole Disk System. You have a choice; you can spend \$50 to \$75 more for a QUALITY Disk Drive with a GOOD Read/Write Head and pay \$10 to \$30 less for each box of Disks, or you can buy the cheap Disk Drive and pay enough more for Disks in the first year to have bought a GOOD Double Sided Disk Drive in the first place. While we are on Disk Drives, let me digress a little bit. You have probably seen the Advertisements in some of the Magazines about the "Screw" Head Movement Drive versus the "Band" Head Movement Drive. Well, the 8" Drives have almost ALWAYS used the Band type of Head control, and they have been the epitome of reliability for years. The Band Stepper System allows much quicker motion, with tighter tolerances, and is less likely to have mechanical problems than the Screw Stepper Systems.

Now to the "Soft" and "Hard" Sector Disks. Almost the whole world has gone to the Soft Sector Disk for all Disk Operations. A Soft Sector Disk has ONE small hole near the inside edge of the Diskette, called the "Index Hole". The Hard Sector Diskette has many holes near the inside drive hole. If the Diskette is a 10 Sector Disk, there will be ten holes which indicate the beginning of each Sector, and another hole with different spacing which is the "Index Hole". If the Diskette is a 16 Sector Disk, there will be 16 holes and an Index Hole, etc. The Hard Sector Disks were popular when Disk Systems first began to be used, but are slowly disappearing from use any more. I don't know for sure, but I would guess that the reason the Soft Sector Disks gained the upper hand was due to the development of Hardware and the flexibility they provide. (For example, FLEX uses 10 Sectors per Track for Single Density operation, and 18 Sectors per Track for Double



Density Operation. Not only that, but Track 00 is ALWAYS written in Single Density Format, while the rest of the Disk is Double Density. This would not be possible with Hard Sector Diskettes.)

At this time, the only Disk System I know of for the Color Computer that uses the Hard Sector Diskettes is the one that was developed by Tallgrass and is now being sold by Cer-Comp. It is a good System, but YOU CAN NOT USE IT FOR ANYTHING EXCEPT THEIR SOFTWARE; you can not run Radio Shack Disks, FLEX, or anything else. Also, NO SOFTWARE written by ANYONE ELSE will run on that System unless it is transferred over to Hard Sector Disks first, which is not easy to do. ALL of the other Disk Controllers use Soft Sector Disks, so the only changes that need to be made to transfer Programs between different Disk Operating Systems is to write a Software Routine that will interpret the way the Information is stored on the Disk to something the other System can understand. To transfer between Soft Sector and Hard Sector Disks, Different Disk Controllers would be required, along with the different Software. You can store a little more Information on a Hard Disk than you can a Soft Disk, because you do not have to use some of the Disk's Surface to Write the Sector Number on it, but the difference is small.

In summary, there are a lot of different types of 5 1/4" Diskettes, but our experience has been that all you need is Single Sided, Double Density, Soft Sectored Diskettes, which can be used with all Disk Systems including 80 Track Double Sided Disk Drives, for Single and Double Density operations.

#### Making a NEW DISK

Now that we have some background information, the first thing we will need to do is make up some NEW Disks to use with our Disk System. When you get a new disk out of the box, it is like a new Tape, in that there is NO Information stored on that Disk. This is no problem with Tape, because each Program is written to it in SEQUENCE, and the only way you have any access to them, other than reading through each one until you get to the one you want, is by the use of the Counter on the Tape Unit. This is not controlled by the Computer, so there is no need to put any "Computer Readable" Information on that Tape (all of the "Formatting" required is accomplished by writing certain Information at the beginning of EACH Program, or File, as it is written to the Tape). With a Disk System, we want the System to manage all of the work for us, so we must write some things on the Disk Surface so that it can tell WHERE the Disk Drive's Read/Write Head is located on the Disk's Surface. Once it knows that, it can then inform the Disk Controller where to move the Head to accomplish the specific task we have "Commanded" it to do. The DOS must be able to tell what portions of the Disk already have Files (Programs) on it, where they are, and what areas are still available for use. All of this Information must be kept on the Disk. Therefore, the DOS allocates a small portion of the Disk's Surface for recording this Information, so that it knows WHAT and WHERE the Information is stored on ANY Disk that is Installed in a Disk Drive. FLEX uses the First TRACK (Track 00) on the Disk for this purpose; Radio Shack uses Track 17.

The FIRST thing we need to do with ANY Disk System is make up some new disks for use with our specific Disk System. Radio Shack can not read FLEX Disks, nor can FLEX read Radio Shack Disks, without special Programs to make the conversion for the different Systems. Not only that, but different Computer Manufacturer's FLEX Systems can NOT read some types of FLEX Disks made by other Manufacturers, such as Double Sided, Double Density; they Write DIFFERENT Information on the Disks (because of the different Disk Drivers and Disk Controllers). TSC specified that the "Standard" for ALL FLEX Systems would be a SINGLE SIDED, SINGLE DENSITY, 35 Track (or 77 Track if it is an 8" Disk) Formatted Disk. This allows a Software Manufacturer to put his Software on a Disk and sell it to ANY FLEX User, and know that he would be able to read it; that User then needs to transfer it to HIS System Disk so he can make normal use of the Program.

There is two reasons for making this transfer. First, the original Disk should be used as a MASTER DISK, and used as little as possible to protect it (in fact, if at all possible, it is STRONGLY recommended that you Write Protect the Disk by covering the Notch on the edge of the Diskette, AND NEVER REMOVE IT; then, it is almost IMPOSSIBLE for something to happen to the Disk when it is in the Disk Drive). Second, you are normally going to be using the Double Density format for normal operations, because you can get a lot more Information on a Disk, and the MASTER Disk will be Single Density.

When we Format a Disk, the Computer instructs the Disk Controller to write Information on the Disk's Surface so that the Disk System can read the Track and Sector that is passing under the Read/Write Head at any instant. It ALSO initializes the Free Sector Map,

Directory, etc., so it can keep track of the amount and location of the Files that are on that specific Disk.

How can we FORMAT a Blank Disk so we can use it? That depends somewhat on the Individual System you are using, but the general procedure is basically the same on ANY System. The Diskette's "Index Hole" gives us a starting point to work from; the Disk System now knows where a certain POINT is on some Track on the Disk. Also, the Computer can tell the Disk Controller to tell the Disk Drive to GO TO any specific TRACK on the Disk, so it starts off by going to Track 00 (the First Track on the outside of the Disk). Now the Computer knows EXACTLY where the Read/Write Head is located. The Formatting Program starts from there, and Writes the Sector Numbers and other Information on the Disk's Surface as it rotates; then steps to Track 01 and does the same thing, then to Track 02, etc., until it has written Information on the whole Disk. This is strictly a timing function; we know the Disk is rotating at 300 RPM, and we know how many Sectors we want to write on each track, so we know how much time to wait between each Sector that we write to the Disk's Surface. In addition, the Computer is going to need some time to "digest" the Information it gets from the Controller, or to gather up what it wants to be written to the Disk, so we need to provide some time for this to take place between Sector accesses. If the Disk System wants several Sectors worth of Information, we want to set up the timing so that about the time the Computer is finished storing a Sectors worth of Information into Memory, the Disk will have rotated to the next Sector we want to Read. This is called "Sector Interleaving". Normally, we will Format the Disk so that we Write Sector Number 1, then skip two or three, and Write Sector Number 2, etc. These "Logical" Sectors will be interwoven among the "Actual" Sectors so that we are not waiting on the Disk to get around to the next Sector of Information that we want to Read or Write.

Once we get all of this written to the Diskette, we then go back and Read that Information. This allows the Program to see if what it told the System to Write to the Disk really DID get written to it. When you NEWDISK a Disk, you will notice that the Drive Steps right along for a little bit, then comes back and tells you it is "Verifying" each Track and Sector, which takes a lot longer. It is reading the Disk and comparing that to what it knows SHOULD be written on it. NOW, all that is left is the "housekeeping"; writing the Boot Information, Free Sector Map, Directory, etc. Should FLEX find a bad Sector, it will notify you and remove that Sector from the Free Sector Map, so that it will not attempt to use it for File Storage later.

OK, now we have the Disk Formatted. Most Disk Operating Systems work with two different types of Disks; a SYSTEM Disk and a DATA Disk. What's the difference? The SYSTEM Disk normally has the "Boot Program" and Commands on it. The Radio Shack Color Disk System contains this Information in ROM on the Disk Controller, so it does not use a System Disk, it only uses Data Disks. All of the other Disk Operating Systems that I am aware of require a System Disk. The "Boot Program" is a short Program that loads the Disk Operating System ("FLEX.SYS" with the FLEX DOS) into the Computer so that it can be used. This short Boot Program is located on the first two sectors of Track 00 on FLEX Formatted Disks. Computers designed to use the FLEX DOS have a short routine in ROM which loads these two Sectors into the computer and transfers control to that Program, which then loads in "FLEX.SYS" off of the Disk. The Color Computer Conversions work a little differently because we are not working with a Disk System that was designed to use FLEX; we use a short BASIC Program which gets a small program which has been stored on Track 17. This Program shuts off the Radio Shack ROMS and turn on the 64K RAM (so we have RAM to store FLEX in), and then loads "FLEX.SYS" off of the Disk. The difference between DATA and SYSTEM Disks is that the Data Disk does not have the Boot Routines, FLEX.SYS, or the Commands, because it will NEVER be used to get the Computer up and running with the FLEX DOS. We will already be running under FLEX (or whatever Operating System) when we use these Disks. Making up a Data Disk simply leaves more room for Program or File Storage, because room is not taken up by System Programs or Commands.

Now that we know a little more about how the DOS works, and how the Disk is Formatted, we can understand WHY this next step in making up a New Disk is so important when using the Color Computer FLEX Conversions. (I will be referring to the Data Comp Version 2.0 of the FLEX Conversions, but you will find similar commands with the others, and for the SAME reasons.) The VERY FIRST thing you MUST do AFTER you have formatted the disk (using the NEWDISK.COM), is run the "MAKESYS" Program. MAKESYS.COM has three functions:

1. It changes the formatting of Tracks 17 and 18 from a FLEX Format to the Radio Shack Disk Format.
2. It copies the information that is in those two Tracks from the Disk containing the MAKESYS.CMD to the new Disk.
3. It adjusts various portions of Track 00 so that FLEX will know that these two Tracks are already used, and will leave them alone.

READ ITEM 2 AGAIN! Note that MAKESYS.CMD copies THE INFORMATION FROM THOSE SAME TRACKS ON THE DISK CONTAINING THE 'MAKESYS.CMD' TO THE NEW DISK. This means two things: one, the Disk containing MAKESYS.CMD must be a "Bootable" Disk (i.e., you can use it too 'RUN' FLEX' and get the FLEX System up and running on the Color Computer); and two, even though you can copy the Data Comp. MAKESYS.CMD to other Disks, they MUST HAVE HAD "MAKESYS.CMD" RUN ON THEM for it to work correctly in making a new Disk. The simple solution is to ALWAYS run MAKESYS from the System Disk you just used to 'RUN' FLEX'. Its operation is also the reason you should run MAKESYS.CMD immediately after NEWDISKING a Disk; too let MAKESYS adjust the FLEX Control Sectors on Track 00 BEFORE you start putting FLEX files on the Disk.

To summarize the procedure for making new Disks for use with the Color Computer Disk Conversions:

1. NEWDISK a blank Disk (or one that you do not need any more information off of - NEWDISK WILL DESTROY ANY INFORMATION THAT IS ON THE DISK).
2. Run the MAKESYS.CMD on the New Disk you have just Formatted from the System Disk you used to get FLEX running.

NOTE: If you only have a single Disk Drive, both of these Programs (NEWDISK and MAKESYS) allow you to change Disks to get the New Disk in the Drive. The NEWDISK.CMD loads the Program completely into Memory, so as soon as it starts asking questions about how many Tracks, Single or Double Density, etc., you can pull the Disk containing the NEWDISK.CMD and insert the New Disk. MAKESYS.CMD realizes the problem when you tell it that you want to MAKESYS the same Drive that it lives on, and it will tell you when to change Disks.

--- RLN ---

## COLOR-IBM

I don't like IBM mainframes. I'm not comfortable in that atmosphere; I know I'm not in control. The University, my employer, has two IBM's linked together. It's just too much. None seems to be able to explain their strange little quirks; they seem to go out of their way to frustrate me. I saw this movie about a bad man who took over the MCP of a computer. I surmised that this must have happened at the University. I had to do something.

I have a Color Computer with 64K and disks. It is a wonderfully capable machine; I'm comfortable and happy with it. Could I use it to take on the mighty IBM's? I could work at home unobtrusively gaining access to their secrets. In order to communicate with them, I needed a modem and a smart terminal package for the Color Computer. I bought Yandy's Modem I and their Videotex rompak. The modem worked fine; the software was lousy. I next bought the disk version of Elgen Systems' COLORCOM/E. What a great package! "Hello, IBM 4341, this is a friendly Color Computer." "Password and ID number; be quick about it or I'll disconnect you" "Log ID, password, ID#." All right, logged on!

But then I got sidetracked. The statistics student in the house was getting frustrated punching and running card decks at the Computer Center; she was forced to wait in line at the printer for her output to see what mistakes had been made. She begged me to figure out a way to write and edit SAS programs from home. That sounded easy enough at the time. We would stay at home and use the Color Computer as a terminal for the IBM.

SAS is the acronym for the Statistical Analysis System, copyrighted by the SAS Institute. It is a terrific package that does everything in the book, including graphs, but is relatively easy to use—with cards. Statistical problems get big in a hurry; often 300K of memory are required for handling matrices. The number crunching requirements can also become formidable. Statistics are not a job for small microcomputers. The IBM 370/158 executes SAS in a batch mode; input is normally with cards. The phone line connects us to the IBM 4341 which operates in an interactive mode, using the Conversational Monitoring System or CMS. So we would start with the COLORCOM software, create programs in the CMS editor and then send them over to the IBM 370 for processing. SAS would have to be fooled into thinking that it was receiving cards, especially the job control cards required in batch mode.

The folk in the Computer Center assured me that it was easy. When they tried to show me how to do it, they could not get the output from the SAS program back to my virtual machine. It just disappeared! "Hey, no problem", my IBM-loving friends at the Computer Center assured me. We'll just put in a destination card to route the output back to the virtual machine; "DEST PEROTTI" will do the trick. For two days the high and low priced help in the Computer Center mused around with my little problem! Nothing. Then, inexplicably, "DEST PEROTTI" ran like a charm on the third day. Typical.

Here is the process writ large. First fire up the Color Computer; load and run COLORCOM/E. Dial up the Computer Center and log on. Use the CMS editor to create the SAS program, saving it on the IBM disk. The first cards in the program will be the JCL (job control) cards for SAS. Submit the SAS program (file) to batch mode and SAS where it will enter the queue and eventually run. The DEST card will redirect the output back to the virtual machine's reader. Get the output file out of the reader and onto the IBM disk for safekeeping. Move the output file to the editor and check out the results. If there are errors, fix the program file and resubmit it to SAS. If the output passed muster, print a copy for yourself at the Computer Center. Then, with the output in the Color Computer's memory, logoff and print it at home to review it. It is pointless to save the output listings to the 80-C's disk, since the output is large and a copy exists on the IBM disk.

Sounds easy, doesn't it? The Color Computer and COLORCOM/E performed flawlessly. The problems exist with the IBM environment; I would guess that the linkage does not work right. The major frustration with this process is that the Computer Center experts do not understand their setup. As a result, when problems arise, they do not know what to do. So together we experimented; we still are experimenting. Maybe this little article can save you a little grief with your local IBM.

COLORCOM/E is said to be a smart terminal package. After I purchased the software, I figured out what "smart" meant. Computer terminals are said to be "smart" or "dumb"; those adjectives differentiate between terminals which have or lack display features, such as screen editing or highlighting. Smart terminal packages for microcomputers indicate those which facilitate interaction with the host computer. Good software permits the user to download to disk or to print what is received from the host. It permits files or messages to be sent to the host. It is able to be configured for the protocols of different hosts. If you dial up Compuserve on a Sunday morning to see what is in the "New York Times", you could read it with a dumb software package. With smart software you could load the paper into your disk, logoff, and print it out at your leisure.

The COLORCOM/E, though relatively inexpensive at \$59, is smart. It includes an Initialization subroutine which sets the parameters for the modem, the printer, and the host computer. Thankfully, a standard exists for 300 baud telephone communication; it is the Bell 103 protocol and even the IBM's use it. The default values for the modem worked fine, except that half-duplex was needed rather than full-duplex because the IBM does not echo back what it is sent. Tandy's Videotex software only runs in full-duplex; as a result you cannot see what you have typed. I had my Epson MX-80 running at 2400 baud, but the maximum for COLORCOM/E is only 1200 baud, so I had to reset the switches. The normal baud rate for 80-C printers is 600 baud; the default is set to 300 baud and must be changed. Also watch out for how many data bits your Color Computer sends to the printer; the early 80-C's sent 7 bits, the newer ones with the Basic ROM 1.1 send 8 bits.

The Initialization subroutine, "Setinit/xxx", has other nice features. My favorite is the ability to define two messages which can easily be sent to the host computer. Since I cannot type under the pressure of the IBM's gaze, I often mess up the log on procedure. With COLORCOM/E I define the messages as my ID, password, and account number. After being connected to the IBM, pressing downarrow (the control key) #7 transmits the first message, downarrow #3 the second. It's fast and error free; nice feature.

Even nicer is the ability to transmit files to the IBM line by line; the file is loaded into the transmit buffer, each line is sent by pressing downarrow #1. Instead of carefully typing in commands for the host computer, a previously prepared file of commands can be sent one by one. This feature is especially helpful for us poor unappointed amateurs, trying to work our way through the IBM labyrinth, where one mistyped character spells doom. Files can be loaded into the transmit buffer even when on-line. With Compuserve, for example, you can quickly get to the location you are seeking. The commands would have been loaded into a file beforehand; when you run COLORCOM/E, you immediately load the file into the transmit buffer. After you are connected with the Compuserve Computer you transmit the commands one by one by hitting control #1 over and over. With the IBM you can create and send a collection of commands to set up the terminal and disks the way you like; you can "CP spool" this, "access disk \*" that, or even input the dreaded Job Control Cards into a program you are creating. To be fair to the IBM, it is easier to create and execute "Exec" files or macros on your disk there. COLORCOM/E also permits a file to be transmitted as a whole package. So, for example, after creating a new file in the IBM Editor, I set the editor to input status and then pressed the control #2 key, transmitting an entire file to the IBM file. Sometimes that procedure worked, and sometimes it literally blew away the Editor and the CMS operating system. I decided that IBM's prefer to be fed in small bites; my file was too much too fast and it made the poor thing gag.

COLORCOM/E uses two buffers of adjustable sizes. The transmit buffer, mentioned above, can either be loaded from a disk file or it can also be written to in the "Advanced Entry Mode". Those wonderful words refer to the mode running in the time period right after power-up, before the other computer is phoned. This mode is also the best way to send commands to your printer, again using control #1. The other buffer, not surprisingly, is the receive buffer which stores everything sent or received during the session. Even with 32K, the receive buffer fills up quickly. Control #4 displays the amount of room left in the receive buffer. Control #3 "freezes" the buffer, maintaining the current contents and "freezing out" newly received stuff from the host. The buffer can easily be dumped to disk, cleared and refilled, etc. After logging off the contents of the receive buffer at your disposal. The up

and down arrows can be used to scroll through the buffer; portions of it can be marked for printing or saving to the disk.

Computer Centers are not nice places. They contain a strange mixture of self-assured priest-programmers and crazed students, mostly the latter. Lots of frustration and anger are exhibited at the Computer Center. Angry graduate students, much fiercer than undergraduates, crowd around the printers, pushing and snarling at one another. They wait. They know how stupid they are; they are fully aware that their program is probably wrong and will not run. They wait for printouts which are snatched from the operator's hands, then passed from one disbelieving student to another. The printout's creator is the one who shrieks and curses. Kiss your Color Computer for saving you from this. Print at home!

Avoiding the Computer Center is what it is all about. Now maybe the cathartic behavior described above is healthy; maybe the Computer Center provides an isolated outlet for hostility and frustration, enabling the graduate students to adapt to a normal existence which is appropriately subdued and obsequious. The fact is that intermingling with them in the Computer Center in their debased condition is frightening and should be avoided. Since all of us are fallible and inclined to computer errors, it is especially sweet to discover one's own folly on the Color Computer screen in the privacy and security of the home. Eventually even IBM programs can be persuaded to run, often creating voluminous output listings. Well, you can have it printed out down by the bestial students or, if you have lots of time and patience, you can print the final results on the little printer that sits next to the Color Computer.

Printers are tricky little devices which have their own microprocessor which responds to funny control characters. The Color Computer doesn't even have a control key, how can the printer be communicated with? The COLORCOM/E package uses the downarrow key for a control key. When the control key is used in conjunction with the 53 keys on the keyboard plus the shift key, 127 different characters or codes are then available to be sent to the printer. Since most IBM programs print out their results with 132 characters per line, the home printer will need to be set for 132 character printing. The Epson Mx-80 is set by being sent a control 0 (letter "O"), which is equivalent to Chr\$(15) in Basic. Since escape is defined by COLORCOM/E as the control+clear key sequence, the condensed print is cancelled with control+clear followed by "F" (enter). Emphasized print, for those of us too poor to afford a new ribbon, is control+clear "E" (enter); double strike is control+clear "G". The Epson printer will print the graphs from the IBM if it is set to 132 characters per line and if you use the IBM Editor to shift the file one space to the left; SAS and SPSS really print 133 character lines.

If you want to get tricky and edit one of the files that you received from the host computer, that too is possible. The receive buffer is saved as an ASCII file by COLORCOM, so both Nelson's Superwriter and Cognitex's Telewriter can be used to edit and format these reports from the big IBM. This is a good way to fix up the results from statistical programs that were not as good as you had hoped. But, since none trusts results which are not printed on the wide green and white sheets, you will have to transmit the edited file back to the IBM for reprinting. Little joke, there.

The above is a terribly simplified account of what can be accomplished by a Color Computer function as a terminal for an IBM mainframe. The IBM mainframes remain under the control of some demonic force; but I have not yet been able to regain control of the MCP for the good guys. I still am occupied by SAS listings which



cannot be true. It is so nice and quiet in my study; I feel protected from the hostile IBM environment. Even bizarre mistakes don't make me angry, I have hope that I will eventually be able to fix them. But I too am a dumb student, unable to comprehend the programs I write and submit. I strongly recommend discovering your mistakes on the Color Computer screen minutes, rather than hours, after submission.

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## COLOR-EXP

### COLOR COMPUTER EXPANSION

When you tire of games and other piddling activities with your color computer, there is a wealth of very useful and serious programs out there that can run on the color computer. For instance, those who have upgraded their color computer to run FLEX™, with either the DATA-COMP or FLK version (don't know about the Spectral Associates version, haven't seen it yet), have the wherewith-all to run almost all of the serious software that the larger 6809 computers run. But there are some serious drawbacks to running heavy software on the color computer. One of the worst is the lack of expansion capability for the color computer. The **General Automation Expansion Interface** helps to solve this important shortcoming of the color computer.

The system sent us for review consist of the two shelf CX-3001A Aluminum Chassis and the CX-2001A Expander Card. These items along with your 32K or 64K color computer, Tandy disk controller and one or two (or three or four) disk drives, transforms the system into a very powerful and versatile computer. You will have a parallel printer port (save the \$125.00 plus for the Epson serial interface) 64K memory access and a buffered expansion bus. All this without hampering the original Radio Shack disk system, cassette port, joysticks or any other regular color computer function. Yep, your color computer can talk to a modem and printer at the same time. Now that is **EXPANSION** with a large E!

### 64K Access Circuit

The Expander Card allows a 32K (Rev-E) color computer to double the useable RAM. However, I should warn you that many of the Radio Shack 32K upgrades use partial defective chips (64Ks that are mapped for the good 32K portion) and you should always run the memory test outlined in a previous issue of 68 Micro Journal, to make sure that all the memory is functional, see April 1982 issue 68 Micro Journal for more on this and the memory test.

For those who have used the Data-Comp CPI 64K upgrade (\$99.95 with complete instructions for all series of color computers) the risk of possible memory failure is greatly reduced, as Data-Comp CPI ships only new, prime, commercial grade, high speed 64K RAM chips. The failure percentage of those who have expanded the Radio Shack 32K to 64K has been rather high, due to their use of graded RAM chips. So if your full 64K is good memory, you now have a **real** computer and at a very attractive price.

For the BASIC programmer this means an additional 64K of RAM for PETK, PUKE and machine language routines. Machine language programs can use all of the 64K memory. Also you will be ready and **able** to run all of the newer languages and software appearing for the 6809 (see any issue of 68 Micro Journal).

The Expander Card parallel port accepts any standard centronic-type device (printer, etc). This frees up the color computer serial port for a modem or other serial device. Also for those who have any of the Radio Shack printers that run with the Model III the Expander Card accepts the printer and cable without any hitches.

For those who desire to interface other parallel devices to the Expander Card the operation is straight forward. It has a 6821 PIA with 8 input and 6 output data lines, as well 2 output control lines and 1 input control line.

### As an expansion bus

It is simple to add other devices to your color computer as the Expansion Bus has buffered microprocessor signals for up to 7 additional peripheral cards. General Automation promises an impressive array of additional devices for this system. Among them are speech systems, A/D converter, D/A converter, TV digitizer and more. If the prices are in line with the price of the basic system, it should make low cost computing available to all color computer owners.

### Furnished software

When you buy the Expander Card you get (source) BASIC compatible line printer software (parallel) FREE. Also provided is a utility program that allows RS BASIC to use the expanded RAM. In addition to the above they provide 3 short demo programs for the system.

### Documentation

The documentation is very complete and even the novice color computer owner should experience NO difficulty in plugging things together and be up and running in a few minutes.

In addition to the software indicated above, the following documentation is included.

#### 1. CX-2001A Installation Guide

This consist of unpacking instructions and complete, step by step, instructions for assembling the Expander Card and disk controller card to the system. They are very complete and include large, well drawn pictorial aids in putting everything in its proper place.

#### 2. CX3001A Expander Card Tech Manual

The technical manual is also complete and covers the following features of the system.

- Memory mapped I/O locations: This list the dedicated memory locations.
- 64K Memory Access Circuit: Explains the 64K feature as relates to the 32K Radio Shack revision-E. This allows the full 65,024 RAM locations to be used.
- parallel Port Technical Description: A run down of the parallel port (printer) input and output capabilities. Includes memory locations occupied by the parallel port.
- An additional section is given for those who desire information on the expansion bus. This covers a technical (but understandable) description of the bus and the header and cable connections.

### Power

The power for the expander card is supplied by the color computers +12 volt supply. Claimed power consumption for the expansion interface and disk controller is well below the Radio Shack maximum stated in Radio Shack documentation. Additional GA expansion and device cards will contain their own power supply and will not further load the computer power supply.

## Conclusions

When we received our system for review we unpacked it and set it up according to the furnished instructions. The total time was about 20 minutes. We plugged everything up and hit the master on/off switch (we use a strip-line cord, everything is plugged into one cord and turns off/on with one switch). The system came on line and everything worked as promised.

The only thing that we do not like about the system is that some hash is picked-up by the added cable lines and gets into the TV. We added the DATA-COMP CPI "SCREEN CLEAN \$39.95" and the hash went away. If the small amount of hash is a bother I would recommend the "Screen Clean". It plugs into the back of the color computer and also isolates the color computer from the TV (could save many bucks if something ever shorted, in the wrong place). It requires less than five minutes to install and does not require disassembly of the color computer.

All in all it is an exceptionally economical way to expand the color computer for more serious computing. The sturdy aluminum chassis is both functional and easy on the eye. The software included leaves little for the average user to do, except have fun. The documentation is complete and better than some we have seen. For quality and price, it is an excellent addition for your color computer. It will allow you to expand and keep abreast as additional expansion devices become available. No doubt we will see others market devices for this system. The special "Introductory" price for the complete system (CX2001A Expander Card, CX3001A Chassis and the CX-2401A Extension Cable) is quoted at \$199.95 as a package. When ordering tell them you saw the "special" in 68 MICRO JOURNAL.

For additional information contact:

GENERAL AUTOMATION  
9600 Roosevelt Blvd., Suite 100-LL  
Philadelphia, PA 19115  
(215) 934-3758

A 68 Micro Journal Lab Review ---

**Editor's Note:** For those who experience the screen hash as mentioned above, the following may help to eliminate the problem, according to an update to the manual received after the review was finished.

"Most of the noise can be cleaned up by proper placement of antenna cable." Also they recommend that the cable between the computer and the TV (antenna cable) be rolled up into a 4" diameter loop midway between the CC and the TV. Then place the loop against the flat side of the chassis. Visible results should be seen. In addition, the loop and the ribbon cable may covered with metal foil to further reduce the interference.

It is our experience that this occurs to many devices attached to the Color Computer. In our office we have noticed that the interference is somewhat different for different combinations of Color Computers and TVs or video displays. We have used B/W and Color TVs, Color Video Monitors and RGB Color Monitors, the difference is noticeable with each. For those who cannot eliminate the interference by the above suggested methods, the 'Screen Clean' is recommended. It should be noted that this problem is primarily a result of the Color Computer and the Monitor, and not the attached unit. And we know many who do not feel that the interference is worth messing with, it is to a degree, a matter of nit-picking.

DMW ---

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## INDEX 68 MICRO JOURNAL 1982

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Note: Many of the items published under the **BIT BUCKET** column were voted the most useful by many readers. These letters, hints and kinks, suggestions and even sometimes (not often) gripes are what has directed us in our selection of articles each month.

We have attempted to select subject matter in relation to the percentage of different readers interest. However, it seems that some subjects have more reader interest, than we have received submissions from you. If we are to publish articles on subjects you are interested in, then we need **your input**. Unlike most

magazines, 68 MICRO JOURNAL is **YOUR MAGAZINE**. This means that you the reader determines what we publish.

We forfeit thousands of dollars of advertising revenue each year because we screen advertised products to insure that they perform as advertised. Some 'bad apples' have slipped through (not many!). However, having tested (we order under an 'assumed' name many items not advertised in 68 MICRO JOURNAL) products advertised in other magazines, we have decided to continue our screening requirements. There are other products that are advertised elsewhere (a very few) that are indeed excellent products. Therefore, it should not be assumed that just because it is not advertised in 68 MICRO JOURNAL it is of questionable quality. I am referring of course to products pertaining to the type computers that 68 MICRO JOURNAL is all about.

So if we are to continue to serve you as we have for the past four years plus, then I need your input on this also. If you have purchased a real 'lemon' let me know. I can assure you we will keep it on file and use the information accordingly. Of all the different groups of computers (Radio Shack, Apple, Commodore, IBM (personal), etc.) we have **less complaints** with our advertisers, than any of the others. We sure do not claim perfection, but I can assure you that you can place more faith in advertising in 68 MICRO JOURNAL, than **any** other computer magazine! We have received hundreds of pounds of letters stating as much, from you the readers.

DMW - - -

## LABEL PRINTER OS9

### CASSETTE LABEL PRINTER

Anyone who makes recordings with a cassette recorder has probably had problems trying to write small enough and clearly enough to fit all the needed information on the small card supplied with the tape.

This program, written in Microware BASIC09 uses an Epson MX-80 printer to print out labels that will fit into the cassette boxes. Each tape has a title that is made up of two lines up to 20 characters long. The title will appear at the spine of the box. Up to 17 selections can be listed for each side of the tape.

There is also space for a tape counter number to help locate each selection on the tape.

After the instructions have been read the program will prompt you for the two line title. If you want to exit the program just type in 'QUIT' in response to the title prompt. After the title is entered you will be prompted for the selections on the first side of the tape. In response to the prompt enter the tape counter number followed by a comma, and then the name of the selection. If you don't know or don't want to use the tape counter number you have two choices. If you just type the comma and the selection name a dash will be placed in the number column. If you type a space followed by the comma and selection name the number column will be left blank. When you have entered all the selections on the first side type 'OVER' for the selection name and the program will start to prompt for the second side. If you have only recorded on one side of the tape just type 'END' in response to the selection name prompt. Using 'END' when entering the data for the second side will cause the label to be printed. After the printout is complete the program will ask if you want to continue or quit.

The printed labels can then be cut or torn on the dotted lines and folded to fit in the box.

The listed version of the program is heavily commented with remarks. After you load the program it can be packed with the PACK command to save space.

Albert B. Accettola Jr., M.D.  
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# PROCEDURE CASSETTE.PRINTER

```

0000 NEW A PROGRAM TO PRINT LABELS FOR CASSETTE TAPES USING AN RS-80 PRINTER
0001 DIM BORDER:DTIME(40)
0002 DIM NUMBER,PRINTED_PATH,DYFEI B:INTERIOR
0003 DIM NAME:DTIME(131) TITLE1,TITLE2:DTIME(200) SELECTION_A:17
    ,SELECTION_B:171:STRING(20) A:171,B:171,ANSWER:STRING
    (3)
0004 LET BORDER="-----"
0005 LET NAME="???"
0006 OPEN APPRINTER_PATH,NAME:WRITE
0007 LET P=PRINTER_PATH
0008 LET NUMBER=0
0009 REM INSTRUCTIONS
0010 PRINT CHR(10); CHR(22);
0011 PRINT " This program is used to print labels that will fit into cassette tape boxes."
0012 PRINT
0013 PRINT " The title is up to two lines of twenty (20) characters. Just type in "
0014 PRINT "QUIT" on the first line to exit the program."
0015 PRINT " Enter the tape counter number 13 digit, a comma, and the name of the "
0016 PRINT "selection. If you don't know the counter number, entering a space followed by "
0017 PRINT "a comma leaves the area blank on the printout. Just entering a comma then "
0018 PRINT "the selection name will cause a ' ' to be printed in the number column."
0019 PRINT "Entering 'END' for a selection name will stop input and print out the label."
0020 PRINT "Entering 'OVER' stops input and starts it again for the second side of the tape."
0021 PRINT " INPUT 'DO YOU WANT TO CONTINUE? (Y/N) ",ANSWER
0022 IF LEFT(ANSWER,1)="" THEN TO
0023 GOTO 999
0024 REM INITIALIZE ALL VARIABLES
0025 LET TITLE1=""
0026 LET TITLE2=""
0027 REM THE NUMBER OF LABELS PRINTED
0028 LET NUMBER=NUMBER+1
0029 IF NUMBER>2 THEN PRINT DP,CHR(12);
0030 LET NUMBER=1
0031 ENDIF
0032 FOR N=1 TO 17
0033 LET A:=""
0034 LET B:=""
0035 LET SELECTION_A:=""
0036 LET SELECTION_B:=""
0037 NEXT N
0038 PRINT CHR(10); CHR(22); REM MORE UP AND CLEAR SCREEN
0039 REM INPUT TITLE
0040 PRINT "
0041 INPUT "CASSETTE TITLE - LINE 1 : ",TITLE1
0042 INPUT " LINE 2 : ",TITLE2
0043 IF TITLE1="" THEN 999
0044 PRINT
0045 REM INPUT SIDE A OF TAPE
0046 PRINT "SIDE A"
0047 PRINT " NO. SELECTION NAME"
0048 FOR N=1 TO 17
0049 PRINT "SELECTION NO. ' ' ' "
0050 INPUT " ,A:1,SELECTION,A:1
0051 IF SELECTION_A:="" THEN LET SELECTION_A:=""
0052 LET A:=""
0053 GOTO 200
0054 ENDIF
0055 IF SELECTION_A:="" THEN LET SELECTION_A:=""
0056 LET A:=""
0057 GOTO 100
0058 ENDIF
0059 NEXT N
0060 REM INPUT SIDE B OF THE TAPE
0061 PRINT "SIDE B"
0062 PRINT " NO. SELECTION NAME"
0063 FOR N=1 TO 17
0064 PRINT "SELECTION NO. ' ' ' "
0065 INPUT " ,B:1,SELECTION,B:1
0066 IF SELECTION_B:="" THEN LET SELECTION_B:=""
0067 LET B:=""
0068 GOTO 200
0069 ENDIF
0070 NEXT N
0071 REM OUTPUTS THE LABEL TO THE TERMINAL SCREEN
0072 PRINT CHR(10); CHR(22);
0073 PRINT TITLE1 " " TITLE2
0074 PRINT
0075 PRINT TAB(10); "SIDE A" TAB(40); "SIDE B"
0076 PRINT
0077 FOR N=1 TO 17
0078 IF SELECTION_A:="" AND A:="" THEN LET A:=""
0079 ENDIF
0080 IF SELECTION_B:="" AND B:="" THEN LET B:=""
0081 ENDIF
0082 IF SELECTION_A:="" AND SELECTION_B:="" THEN 250
0083 PRINT A:1 TAB(15); SELECTION_A:15; TAB(35); B:11 TAB(40)
0084 IF SELECTION_B:1
0085 NEXT N
0086 REM PRINTS THE LABEL ON THE PRINTER
0087 PRINT DP,BORDER
0088 PRINT DP,CHR(27); "B" CHR(17); REM SET EXPANDED PRINT MODE
0089 PRINT DP,TITLE1
0090 PRINT DP,TITLE2
0091 PRINT DP,CHR(27); "B" CHR(10); REM CANCEL EXPANDED PRINT MODE
0092 PRINT DP,TAB(17); "SIDE A" TAB(27); "SIDE B"
0093 PRINT DP,CHR(15); REM SET CONDITIONAL CHARACTER PRINT MODE
0094 PRINT DP,CHR(27); "U" CHR(11); REM UNIDIRECTIONAL PRINTING
0095 FOR N=1 TO 17
0096 PRINT DP,TAB(10); B:15; TAB(35); SELECTION_A:15; TAB(40); B

```

```

0097 (H) TAB(43); SELECTION_B:1
0098 IF N=12 THEN PRINT DP,CHR(19);
0099 PRINT DP,BORDER
0100 PRINT DP,CHR(27); "B" CHR(11);
0101 PRINT DP,TITLE1
0102 PRINT DP,TITLE2
0103 PRINT DP,CHR(27); "B" CHR(10);
0104 PRINT DP,BORDER
0105 PRINT DP,CHR(15);
0106 PRINT DP,CHR(27); "U" CHR(11);
0107 ENDIF
0108 NEXT N
0109 PRINT DP,CHR(10); REM RESET PRINTER TO ORIGINAL STATUS
0110 PRINT DP,BORDER
0111 PRINT DP " PRINT DP " PRINT DP
0112 INPUT "ANOTHER ONE? (Y/N) ",ANSWER
0113 IF LEFT(ANSWER,1)="" THEN TO
0114 PRINT DP,CHR(12); CHR(10)
0115 999 END

```

SYMPH. No. 5, & No. 8 BEETHOVEN	
SIDE A Symphony No. 5 in C min. Op. 67 Vienna Philharmonic Orch. Hans Schmidt-Isserstedt	SIDE B Symphony No. 8 in F maj. Op. 93
000 1st movement Allegro con brio	000 1st movement Allegro vivace e con brio
2nd movement Andante con moto	2nd movement Allegretto scherzando
3rd movement Scherzo	3rd movement Tempo di marcia
4th movement Allegro vivace	4th movement Allegro vivace

SYMPH. No. 5 & No. 8 BEETHOVEN	
Allegro	440 Sonata No. 23 in F min. Op. 57 Appassionata Robert Casadeuss
507 Sonata No. 19 in C# min. Clair de Lune Robert Casadeuss	508

## RUMORS & SUCH

As has been our practice in the past, we try to keep our readers informed on what is likely to happen, in the 68XX world. Sometimes these 'tid-bits' cause 'howls' from vendors who would rather do their own 'announcing'. However, since we publish 68 MICRO JOURNAL for the benefit of our readers it is our responsibility to try to keep you as informed, as we can, and still not do real harm to anyone.

We have respected many request for reasonable delay in publishing information, that might tend to negate the efforts of some manufacturer. For one, those instances that would allow a competing manufacturer to know what his competitor is about to unveil, or has on the drawing boards. That of course would do more harm than good, and be quite unfair on our part. Especially as many are personal friends and often pass information, of a secret nature, during the course of casual conversations. In fact, that has been a real quandary. So if occasionally some bits of interesting information are delayed in getting publishing, you know the reason. However, we still get the jump on about everyone else, and manage to keep you informed.

Possibly by the time this gets into your hands, Radio Shack will have announced the availability of MICROWARE'S OS9 disk operating system. My information which comes from a source close to Radio Shack, tells me that it will also be used on other 6809 computers, to be and being developed by Radio Shack. What this means to the 6809 market is somewhat unclear at this time, but it should be a beneficial stroke for other 6809 system manufacturers.

Now that may sound strange. But if you will stop and think, it becomes evident that Radio Shack will, by the very size of their distribution outlets and efforts, make more potential computer users aware of the 6809 and all it has to offer as a CPU, than any other 68XX manufacturer I know of. They have the advantage of sheer market weight.

It is not intended as a slur towards Radio Shack to say that although they produce excellent computers, they do not produce computers as versatile as most existing 6809 computer manufacturers. In fact, it is no secret that Radio Shack sells only a very small portion of the 'add on after-market' items, normally associated with a more complete computer system. Also it is a fact that many potential users came into the small computer market as a direct result of Radio Shack computers. A very large portion of these users changed over to other brands of computers, as their needs exceeded the availability of Radio Shack brand add-ons, or in some instances better and less costly than Radio Shack brands were available. The built-in restrictions, both hardware and software related, of their computers, sent many users off looking for computers that would allow them expansion and more access to current state-of-the-art add-ons, than available through or for the Radio Shack computers.

In the realm of the 6809 this same market activity will probably exist, and many who come into the small computer user market via Radio Shack products, will in many instances look for bigger and better 6809 machines, as their needs expand. When that time arrives, those existing 6809 computer manufacturers that we now know, will have the equipment to completely fill these needs. We have observed this already in Color Computer users.

So it is somewhat a paradox, but most 6809 computer manufacturers will actually be aided rather than damaged by the entry of Radio Shack into the 6809 marketplace.

We will be seeing a UNIX™ type system also for the Radio Shack 68000 Model 16 computer. It will come from a well known software house. None too soon either. The present availability of software for the RS 68000 machine is at a very low ebb and can go nowhere but up. However, I expect a rather short life for the Model 16, as it has some rather stiff and unnecessary limitations. It does little to make available the real power of the 68000. It is restrictive in both RAM addressability and add-on capability. These two alone cripple what could have been a fine computer (more on this in a later article).

However, to get back to the point; there will be additional 68XX computers from Radio Shack. If the present 68XX manufacturers keep their R&D aggressive they should reap the fallout.

#### COLOR COMPUTER INFO EXPANSION Volunteer Column Editors Needed And our 'Annual Progress' Report

For the past 6 months or so 68 MICRO JOURNAL has experienced an **explosion** of 'new' Color Computer subscribers. For this we are indeed delighted. Not only for the subscriptions, but because it means that many new 6809 users are 'coming aboard'.

For our advertisers this is good, for it promotes the 6809 CPU, as the finest available! Once a new user discovers what we have had all along, he is a very real potential customer for a larger 6809 system. Hence we all profit. I am told practically daily by some 'older' advertiser that the influx of new 'color computer' users has enhanced their sales. Some of course to a greater degree than others. However, all stand to gain, as it broadens our user base. And lets them know what is available, once they experience the power and ease of using the 6809.

Now we, 68 MICRO JOURNAL, have come to the point that we need to more fully support this new base of readers. We have, in some respects, a different type of user, than we started out with. It seems that the new users, coming in at the color computer level, need more basic information than has been our normal practice in publication. He needs solid information on the more popular disk systems, such as FLEX™, UnifLEX™, OS9™, SSB DOS™, SOOS™ and possibly others. Especially FLEX™, UnifLEX™, and OS9™ which seem to be used by more than 80% of the larger 6809 systems. As some of the other systems become more popular, they should be supported also. This means that we must have more basic articles to satisfy this need. Also we will be publishing more 68000 articles, and soon. Meaning that we need volunteer editors to do columns to help us all.

If you feel that you could handle a column every other month or so, **PLEASE DROP ME A LINE.**

I hope that we can soon enlarge the size of 68 MICRO JOURNAL to allow more coverage of the subjects we are now light on. Probably we will have to begin publication of an additional magazine. Similar to 68 MICRO JOURNAL but more fully covering the color computer and other new 6809 computers coming soon.

Let me know your feelings on this and also let me know (soon) if you want to participate. Either way we are going to expand our coverage, but we need your input if it is to succeed, as has 68 MICRO JOURNAL.

As of the end of 1982, 68 MICRO JOURNAL covers more of the 68XX market than all other magazines combined.

Advertisers tell me that the only advertising results they see is from their advertising in 68 MICRO JOURNAL. Of course there is a reason, mainly that we have more than **10 TIMES** the subscribers, than any other 68XX magazine. And that is a **FACT** that makes the cost of advertising in 68 MICRO JOURNAL about 1/6th to 1/8th of what it is in other 68XX magazines, per reader. And readers is what **COUNTS**. Therefore, our success means a bigger and better magazine for all of us. So please, when you order something from one of our advertisers, tell them you saw it in 68 MICRO JOURNAL.

We will not be 'sitting on our laurels' in the coming year. I can promise you an expanded and more complete 68XX magazine, as we get our expansion program into gear. I tell you this now, however, you already know, 68 MICRO JOURNAL's publishing policies are set by our readers, and the contents of 68 MICRO JOURNAL is furnished by our readers, therefore, as usual, I am just letting you know. Together we will get it done. To all of you our most **HEARTY THANKS!!!**

DMW - - -

## QED

### QED A ~~do-it-all~~ EPROM BURNER

As time passes it seems, and certainly not a day too soon, that the hardware and software we receive for review looks more and more like **professional** and less and less like hobby. This is a good sign, and looking at all the new systems coming out each month, one wonders where the Standard S50 Bus is headed? Well, I can tell you one thing for sure, it will be around for a long time!

There is just one thing that most of the newer offerings, and a lot of the old ones, can't do; let the user modify the way the 'beast' functions. I have heard too many Apple, Radio Shack, IBM, Cannon, Commodore and users of other brands wring their hands and literally cry because of **built in restrictions**. For some unknown reason I even feel a twitch of remorse when I hear of one of the old (established??) S100 bus manufacturers going down the drain. Actually they had a 'millstone' from day one, namely a bus that had more noise and other problems that was just too much to overcome. However, they did allow the knowledgeable and brave a 'chance' to change the system. The newer stuff just won't let you even try.

Granted that most of the newer users of Standard S50 Bus computers don't do as much 'piddling' as we did years ago (golly, seems like a long time ago) but for those that have the knowledge and skill, the equipment and where-with-all is available. We have that over most all other computer systems. Not only do we have it, but it is mostly **TOP GRADE!**

This review is about such a product. A PROM burner limited only by your skill and equipment. And the rather nice part about this particular 'burner' is that really not much skill is required. It will program any popular (5v) EPROM available; 2508, 2758, 2532, 2732, 2732A, 2516, 2716, 2564 and 2764. All that with a commercial grade EPROM burner that sells for about, well, exactly \$125. Yep, \$125 and designed to run continually, no wait and cool off states.

### UNIQUE Technologies QED EPROM Programmer

This unit fits on the S30 bus of a standard S50 computer. LEDs monitor both the programming and logic voltages during operation. Equipped with a ZIF 28 pin easy insertion socket for both 24 and 28 pin EPROMs. And for maximum safety all pins of the EPROM socket are diode clamped to within one diode drop (approx. .6v) of ground potential.

### SOFTWARE

The programmer is driven by menu type software called QED. This software is very user friendly and short of overlaying it with data, or a 'run away' seems difficult to crash. At practically each stage of operation the user may continue or abort, without fear of scrambling the EPROM or other RAM data. Even little things like accepting both upper or lower case characters (most commands to the system are single characters), not having to type the C/R each time, makes things go nicer. Another nice touch is program 'feedback'. One of the most annoying things about using a computer is when you command it to accomplish some function and the cursor either just sits there, or the screen goes blank with the cursor just sitting there (sometimes not) and you

wait and wait and wait.....not really knowing if it is doing it's thing, or suddenly decided to send the 'PC' off on some bit twiddling rampage among the system RAM. A nice thing about this software is that it **tells you** what it is doing, or in other words it issues **progress messages**. Someday I am going to write a manual for those folks who write software. And if I don't get anything else across, I hope that they will design their product so that it always keeps the user informed as to what is happening way down in the bowels of RAM. The old saying, "no news is good news", does not apply when using a computer. I want to know what is happening and what to expect. No data is allowed to roll off the screen until the user is ready. Seems like such a little thing, but to us who still meddle with the 'Innerds' of our computer, it makes life a lot easier.

#### HARDWARE Requirements

The QED system adapts to most 6800 and 6809 systems. The requirements are as follows:

1. A disk system 6800 or 6809 with a 30 pin slot.
2. Disk operating systems FLEX™ or SSB™.
3. An ACIA based terminal or a system that simulates an ACIA based terminal.
4. A CRT or terminal of 24 lines by 80 characters, with data rates of 4800 baud or faster.
5. The display should support home cursor, clear screen and backspace.
6. At least 6K of user RAM plus room in additional RAM for the data to be programmed.

The only real fiddling with the software is getting the 'delay' time correct for your particular system clock. Once done, and assembled, no changes should be required. That is provided you do not change the terminal address (sorta difficult on most systems) or change the QED board to another S30 slot.

Four (4) procedures are given for figuring your system clock **exactly**. While I won't go into them in detail I will tell you that it really isn't too difficult.

If you know that your system clock is exactly 1.0, 1.5, or 2.0 MHz, then the supplied figures will do nicely. However, if your system isn't running at the speed you **believe** it is, then this is a good excuse to check your phase 2 clock.

Method 1. Values are given for the speeds above.

2. Approximation: Here all you need is a digital watch or clock. Just assemble and run the furnished program 'SIGHT'. This method will get you within 2% of your actual system clock speed, and should do fine as most manufacturers require an accuracy of 10%.

3. Calculation: this method requires a frequency counter. However, it will get you closer than method 2, but probably won't do any better.

4. Direct: This method requires an oscilloscope. While this method will get about as close as method 3, again it is probably not any better than method 2.

#### PROGRAMMER Software

The furnished software, in source code, is called QED9, at least it is for the FLEX™ (6809) version we are reviewing. It may be 'ORGed' at any place in RAM you might desire and is relocatable (6809 only). One thing we were **really** impressed with is that it is **completely commented, completely**. This to us is very, very important. Too much supposedly 'commented' software we see is lacking in areas that apparently the author felt were so simple that that particular portion of code needed no comments. Some programmers are so proficient that they lose sight of the simple fact that not all users understand it as well as they do. I feel that I write fairly clean code, however, there have been times while modifying code that was supposed to be 'completely' commented, that I had to stop and do some paper bit twiddling to figure what to do next. For some that can spell disaster. These source programs are a **joy** to read. Just like a good book (right, Mickey??), or was it newspaper?

The necessary information needed to be placed in the source before assembly by the user is as follows:

The address of the S30 slot where the board will be positioned.

The address of your terminal ACIA. (Video boards that simulate an ACIA such as the FEBE's VIDEO PORT can also be used, PIA driven video boards will require user interface).

The backspace character for your system, this is by standard the code 08, but on some systems beware, it may be different. If it is you can insert your backspace character.

The address of your monitors warmstart routine.

Instructions are included on how to determine most monitor warmstart addresses.

Also you will be required to furnish the warmstart address for your disk operating system.

The FMS CLOSE routine needs to be inserted.

The 'Utility Command' starting address is needed.

The 'delay' mentioned above needs to be inserted in the source code also.

The number of lines on your CRT or terminal is needed so that messages may be not only centered but not scrolled off before you are ready.

The address where you desire to place the data to be programmed.

The final information needed prior to assembly is the characters needed to cause your CRT or terminal to do a 'home up and clear screen' function.

The above may seem complicated, but in practice it is simple and took about 15 minutes of looking up values and addresses in our 'FLEX users manual'. SSB users should have it as easy.

#### OPERATION

Operation is simple due to the interaction from the menu. The first menu is the 'MASTER MENU'. Here selection is allowed for calling any of the following actions required for the particular burning session.

Load buffer with a constant. Examine or change buffer. Load buffer from memory, load buffer from EPROM, relocate this program, execute other disk commands (must not overlay main program or data buffer), alter buffer origin, hex dump of buffer, verify EPROM programmed, verify EPROM erased, select EPROM type (used to burn an EPROM different from the one originally specified while assembling the furnished source code), program EPROM, exit to the monitor or exit to the disk operating system.

The Master Menu also informs the user of what type EPROM is expected by the system, as called. Also the data buffer is shown from starting to ending address, along with a copyright blurb, version numbers, etc.

#### DOCUMENTATION

The documentation furnished is complete, even to including a sample programming session. Each of the menu selections is covered in detail. The average user will experience little if any difficulty using this system. The 'advanced' portion (you know, back a couple hundred words, where I told you about the information you furnish the source code prior to assembly) is detailed to allow a completely custom version for your particular requirement. Most EPROMs and other changes can be declared at 'burn' time, but for our needs a custom program is better. This way we just drop in a clean EPROM, verify that it is clean and let'er-rip.

#### THE BOARD

The S30 slot board is of high grade epoxy glass. It is solder plated and silk screened with each component marked. All ICs are socketed and ours came with gold plated bus pins which are standard.

#### The Personality Module

For each type of EPROM, 2708-2564 (16K 2528 and 27182 types will also be supported), there is a 'Personality Module' mated into a 16 DIP IC socket. This unit straps the EPROM socket to a portion of the main circuit board, allowing the different types to be programmed. It is a simple matter to make your own (instructions included in manual). Normally furnished module is a 1K and 2K module.

#### Conclusion

This board is ideal for both occasional use and heavy duty applications. The system requires no 'cool down' period between EPROMs and does not unduly load the system power supply. However, users of older systems with light power supplies might want to insure that their system, if 'loaded' with other boards, is capable of maintaining adequate power to the rest of the computer. Special attention should be given to the +5V (or so) source supply, as this is the one that normally loads down first. This applies when any additional board is added to the system.

Also I was told by Richard Rae, of UNITEK that should any customer experience problems configuring the system, that they would be willing to configure the system at **no charge**, provided the customer could supply the necessary information. Most standard systems would require little knowledge on the customers part. Which



makes for a super package, and the price is right.

Additional information or to order contact:

UNITEX  
PO Box 671  
Exton, VA 23847

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## PB4-BUFFER

### PB4 Intelligent Port Buffer

A few months back we did a review of a new PROM/RAM board by ACORN Computer Systems. The board was a new and novel addition to the Standard 550 Bus systems. At that time they promised some additional 'new and novel' boards for our bus. Well, here is another.

The PB4 is an intelligent port buffer. What this essentially means is that it does its own 'thinking' and also stands between the computer and whatever it is attached to. In our case a parallel dot matrix printer (MX80).

It should not be confused with a spooler. FLEX already has a spooler. A spooler is a multi-tasking program that allows, in the case of FLEX, a printer to be printing out a file or listing, while the user regains control of the keyboard and computer, and continues to do other tasks. This has the restriction that no other printing task can be started while the spooler is running.

Now a 'Port Buffer' is another thing. A port buffer absorbs speed differences between two objects, our case a computer and a printer, and by storing all or most of the task to be buffered, and also having its own thinking system (in this case a 6802 CPU), along with necessary coupling devices (our case PIAs) and other associated digital devices, it drives the printer and the operator regains control of the keyboard and computer. So like spooling we can do another task while the printer is printing out a file or listing, etc.

The primary advantage over a spooler is that the port buffer allows things to run faster and without delays and interrupts to the disk system. There are two delays that the PB4 will eliminate. First, the printer is much slower than the computer. Therefore, keeping the printer running during disk operations and keyboard inputs eliminates one. Second, during data output the computer outputs lines of characters, a carriage return and a line feed. The printer will accept characters up to the carriage return. Upon receiving a return this line feed is then printed. The printer will not acknowledge this line feed or release the printer until the line is completely printed. The 'port buffer' having stored these control characters, as received, continues to drive the printer, from its memory, thereby releasing the computer for other task.

The PB4 also entails two extra buffered control lines at an output header. These lines can control the printer, or any other device to be driven by the on-board PIAs.

There are two versions of this board. One, like ours, is used as a buffer. However, it can function as a 'stand alone' 6802 computer. It comes with 4K of RAM, but instructions are included to allow expansion to 8K.

It comes as bare board, kit or wired and assembled.

There are extra pads on the board. These allow strapping the board for all those functions necessary in single board computer operation, including master reset, etc. Basically it consists of a 6802 CPU, 4K (expandable to 8K) of RAM, a 2K monitor and 28 I/O lines.

A universal PB4 is also available, in kit or assembled form. This board trades one of the PIAs and the Molex connectors for a reset timer circuit and a 24 pin input header. The control PROM (monitor) is a 2716(5v) EPROM and the RAMs are 6116 types.

### Documentation

As usual the documentation is good. It even includes a section on proper soldering (ala Heath). Also included is complete construction instructions and drawings and circuits. A battery backup option is explained for those needing this feature. A real 'bug' to many who construct kits, where 'handshaking' is used, is the lack of much information concerning handshaking. This is covered. In fact, to adequately describe the documentation, it would be practically necessary to reproduce the entire manual. Needless to say, but the more they tell us, the easier it is. This board should be no problem to the average kit builder type.

### Software furnished

Because most users will use the PB4 to drive a printer they have included source listing (commented) for Epson type printers, including the 'hard reset' required by earlier Epsoms. Also included is a source program, in 6800 that is 'Centronics' compatible. This program is the board driver routines. Also it does a NMI self test and a RAM test and report to the printer, for the PB4 board.

### How it works

As stated earlier, the PB4 is a 'Single Board Computer' in its basic configuration. The 6802 uses the normal Motorola type mnemonics and has 128 bytes of RAM at \$0000 to \$007F. A battery backup option is provided for the first 32 bytes of RAM. The RAM sockets may be jumpered as either all RAM or EPROM. By piggy-backing the memory, you can have a total of 10K or PROM or 8K of RAM and 2K or PROM.

Twenty-eight (28) I/O lines (input/output) are provided. This consists of 8 buffered input lines, 8 unbuffered programmable I/O line, 8 buffered output lines and 4 buffered control lines. The control lines are 2 output only, 1 input only and 1 programmable. All lines are non-inverting.

There are 2 PIAs. One PIA is the interface between the 6802 CPU and the 530 bus. It is not used in the stand-alone mode. A 74LS244 is the input bus driver. It is a tri-state device and is controlled by the 6802 CPU. The other PIA is the output device with its lines buffered by 2 8T97s. Handshaking is the standard Centronics type and is accomplished by CA1 and CA2 of the PIA.

Input handshaking is somewhat more complex. CA2 on one PIA is connected to CB1 on the other PIA. When your computer sends a "data sent" to the board an IRQ is done by the 6802 CPU. The CPU toggles the other PIA when the IRQ is output by the CPU. This allows complete handshaking between the board and the computer. By this method the 6802 CPU continues to service the output device, normally a parallel printer.

### Conclusion

The PB4 is constructed of 2 oz. glass-epoxy copper board, double sided with plated through holes and solder masked.

For the user who 'spools' large amount of data to a printer or other device, this system can save a lot of time. Also I had stopped using the spooler in FLEX due to the starting and stopping of the printer and the disk system, as the parallel task accessed the disk system. This causes more distraction, at least to me, than normal spooling is worth. We do more text editing on some of our 6809 systems than any other, or all other, task combined. Being able to output to the printer and not having the printer stopping and starting, is well worth the price of the system. It is that every time the printer stops, due to a disk access normally, I stop also, losing time and most often forgetting what I was about to type next. The PB4 board just lets the printer hum on and on, never pausing or stopping.

A note from Marie Giesfeldt tells me that they will also be coming out with an "Extended Addressing System". It is claimed to transfer 20K in .25 seconds. To go along with this system will be a 168K PROM board and a 2764 PROM burner. Will review this system sometime in the near future.

For Additional Information contact:

ACORN Computer Systems  
11931 W. Bluemound Road  
Wauwatosa, Wisconsin 53226  
(414) 257-0300

For current prices of kits and assembled boards see advertising, 68 MICRO JOURNAL.

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## AUTO-COMM

### AUTO-COMM A "SMART" Modem Program & Hardware

One of the more popular additions, to the computer both at home and at work, has been a telephone modem

and appropriate driver software. Here at CPI (Computer Publishing Center), headquarters for 68 MICRO JOURNAL, we have installed a modem system, using the Thomas Instrumentation **MODEM BOARD**. It has been on line for a few months now and already hundreds of readers (and others) have used the system, passing both messages, suggestions, complaints (not many), source (text) files and downloading to their systems source files from our modem system. We will soon install another 6809 system to handle files to be downloaded from authors and Associated Editors from their remote (out-of-town) sites. We will be using the system described in this review. See **SYSTEMS designware** advertising.

### OVERVIEW

Auto-Comm automates many of the tedious aspects of working with a time sharing main frame computer.

1. Automatic dial-up of other computer systems.
2. Copy files from other computers to your system and store on your disk system, if desired.
3. Send a file from your system to another system.
4. Send "special" or frequently used messages automatically (auto-sign-on, etc.).
5. Conveniently change communications formats (UART formats, etc.) and control echo/no echo.
6. Print during a run, or later.
7. Send electronic mail to other computers, provided they are equipped with Auto-Comm or other similar program.

### Features

25 commands, conveniently accessed through 'HELP' functions. An easy-to-use single mode system, with friendly command interpreter.

### System Hard/software Requirements

Auto-Comm runs on a 6809 computer under the FLEX™ disk system. It requires 16K or more of available RAM. And a serial port such as the SWTPC S-2 Serial Card.

Also required, of course, is a modem to connect to the telephone lines.

A 'pulse type' dialer is required if 'auto-dialing' is desired. The documentation includes complete instructions for constructing a pulse dialer, consisting of 4 transistors, 2 opto-isolators, 1 zener diode and 4 power diodes, standard (type 1N4002 or similar). Of course if you do not need the 'auto-dial' feature, then the dialer can be forgotten.

Also to use the 'auto-dialer' 2 parallel lines are required, such as from a PIA.

Auto-Comm was originally designed for the SWTPC S/9 6809 computer systems with the MPID board and a S-2 serial port, otherwise a parallel port must be made available and the software patched to reflect the address of the parallel port. Complete instructions are provided and are little cause for concern for most users.

### Using AUTO-COMM

In normal operation Auto-Comm is quite simple. Little reference to the manual is needed as Auto-Comm has a 'HELP' mode and also prompts the users in such manner that operation is hassle free.

All commands are organized as a 'tree structure' and the 'HELP' command is the root, the secondary HELP commands are intermediate nodes, and the functional commands are the leaves. Although this may seem complicated, in actual practice it is of no concern to the user. It does however, from the efficiency aspect of the binary code (program) help to insure fast and error free operation.

The command (,HELP), all commands start with the comma (,), will produce the following screen display of secondary commands:

```
,MSG
,SCREEN
,CALL
,FILES
,FORMAT
,EXIT
```

Typing a command will list all commands within this group, along with a brief description of each.

Typing (,SCREEN) will display all 'display' commands, and typing (,CALL) will display all 'dialer' related commands. The list of commands is extensive, but not overpowering. The user soon feels right at ease with the command structure and will appreciate the 'user friendly' aspect of the system.

Command ,HELP prints the command group, as indicated above. Command ,MSG prints the following message related commands:

```
,LISTM Display your prepared messages.
,SENDM Transmits a prepared message.
,Sn Transmit message n.
,LOADM Load message from a file.
,SAVMSG Save messages and phone numbers on disk.
,SETMSG Enter a message into a message buffer.
,BREAK Send a one second break.
```

Command: ,SCREEN

```
,ECHO Turns screen echo on.
,NOECHO Turns screen echo off.
,ERASE Erase all text buffers.
,PRINT Turn printer on.
,NOPRINT Turn printer off.
,LF Add line-feed after receiving C/R.
,NOIF No line-feed after receiving C/R.
```

Command: ,CALL

```
,LISTPHN List all recorded (saved) phone numbers.
,LOADPHN Load phone numbers and messages from disk.
,DIAL Dial a recorded phone number.
,SETPHN Enter a new phone number in a phone buffer.
,SAVPHN Save phone numbers and messages on disk.
,HANG Disconnect from phone line (hangup).
```

Command: ,FILES

```
,SAVETX Save text file to disk.
,SENDF Send a disk file through modem.
,CLSAVE Close disk save file (abort).
,CLSEND Close disk sending file (abort).
```

Command: ,FORM

Prints a list of UART data formats (8 allowed).

Command: ,EXIT

List the exit command.

### Special Commands:

The ,FORMAT command allows the user to change communications formats (7 or 8 bits, even or odd parity, local keyboard echo and other necessary data). ,FORMAT is entered and a special FORMAT menu is put to the screen, the group is listed and selection is done by number. ,FORMAT is so structured so as to make the accidental destruction of good format data difficult.

### Program Structure

Auto-Comm loads to low memory and uses all memory up to the 'End of Memory' pointer in FLEX™. The 'text buffer' is set up as a circular buffer. Different pointers maintain buffer data such that it appears as a separate print buffer (hard copy), and another for saving data to a named text file for disk storage.

Auto-Comm requires 8K of RAM and uses an additional 4K for messages and I/O buffers. The remainder of free RAM is devoted for text file storage. Data received but not needing to be saved can be flushed from the buffer by the command ,ERASE.

Received data that is to be saved to disk is handled by the ,SAVETX command. The sending computer must respond to XON/XOFF commands. If communicating with a computer that uses non-standard XON/XOFF symbols or commands, the change may be made, to Auto-Comm as necessary. If the XON/XOFF characters are different between Auto-Comm and the other computer, or the other computer does not use any type of XON/XOFF, Auto-Comm will detect this and limit incoming files to text buffer length.

Binary files may be exchanged, however, they must be converted to hexadecimal code. This allows XON/XOFF to function.

Auto-Comm documentation includes complete instructions, in addition to those previously mentioned to accomplish software or hardware modifications as required by most non-standard 6809 systems.

### Conclusions

Auto-Comm is a very user-friendly system. Designed for the SWTPC S/9 series of computers, it adapts easily to practically any other 6809 computer having the necessary port functions as mentioned above. Like any other serious software it looks more complicated than it actually is in practice.

For those desiring a complete modem program then Auto-Comm is a must. The documentation is complete and the necessary hardware (pulse dialer) is simple to construct (pre-wired dialers may be available soon) and install.

For additional information or to order contact:

**SYSTEMS designware**  
6712 E. Presido St.  
Scottsdale, AZ 85254  
A/C 800 272-4817 - AZ 602 991-1657

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# PERFECT NUMBER

Name: INTPROV.FBI  
Purpose: Various interpreters for the 6809

John C. Braggfield Oct. 11, 1982  
1803 Capgem  
San Antonio, Texas 78220  
512-346-2072 at home.  
Dear Mr. Williams,

Enclosed are several interpreters for use on the 6809. Several have been implemented and tried out with several years experience. I find the 6809 better than the PDP11's and plot cheaper than the VAX for this kind of work (PDP & VAX trademarks of Digital Equipment Corporation).

All these interpreters have a common stack interface or discipline. By making all major subroutines use this interface, debugging and coding go much easier. Subroutine calls are consistent.

This effort is primarily experimental and being pursued as a hobby. As a programmer and engineer with several years experience, I find the 6809 better than the PDP11's and plot cheaper than the VAX for this kind of work (PDP & VAX trademarks of Digital Equipment Corporation).

For a more theoretical treatment see pp31-34 of the June 1982 issue of Computer Architecture News published by ACM.

All included in the floppy is LAMPROM.FBI which will generate all the machine perfect numbers or all the known Mersenne primes. LAMPROM also has the TC interpreter. It is not the most efficient, I believe a scaling factor of 6 is possible.

There are three routines that must be implemented for each type of interpreter. NEXT is the code following every machine code sub-routine which goes up one level to find and execute the next machine code sub-routine. DSD is the code preceding every machine code interpreter string which where the interpreter and causes the following string to be interpreted. Each such string must end with an ASD code which is interpreted to cause return to the string one level higher. ASD is used for "descend" or "call". ASD is used for "return", and NEXT comes from "forth" or "next".

U is the value stack pointer  
S is the return stack pointer  
T is the code scanner  
X & Y can be used in subroutines, but are sometimes used by the interpreters between subroutines. This forces all parameters to reside on the value stack and results returned on same.  
Direct page is set to the page containing the DSD and ASD routines.  
This is an optimization to allow direct page jumps to several very common routines.

For TC, TC2, and CTC a "JP" code is the first code of the TC, TC2, or CTC code string. The code for ASD is the last code of a TC, TC2, or CTC string.

Threaded code (TC) -- often used in Fortran's DSD. PWS T save code scanner  
LDX -2,T get code of the JP DSD  
LEAT 2,X advance past the JP DSD and place in T  
next

NEXT is short enough to be used inline in all machine language routines using MACRO  
JP T, next  
LDX  
ASD PULS T the address of ASD is the last code of a TC string  
next

Indirect threaded code (ITC) -- often used in Fortran's DSD. PWS T  
LDX X  
LEAT 2,X  
next

also short enough to be used inline next routine  
LDX T, next  
JP (X,X) X has code of symbol entry area  
UNCM  
ASD PULS T the address of the address of this ASD is the last code of a CTC string  
next

Compressed threaded code (CTC) -- often used in Pascal's and Basic's DSD. PWS T  
LDX X  
LEAT 2,X  
next  
next MACRO  
JP T, next  
UNCM

"X" is not so short in this code, so the code is to the direct page where it can be reached easily. LDX (PWS), address of jump table  
LDX X  
ASD  
JP (X,X)  
ASD PULS T the jump table must have an entry for this routine  
next

Machine code (MC) -- the traditional route DSD. Comp with the JSH or BSA last opcode  
next MACRO  
ASD RTS

What all of this implies is that a coding standard is possible for the 6809 which still allows the user his choice of interpreters, while sharing subroutines and code with users of other interpreters. The standard or discipline is:

All arguments and results are passed on the U stack.  
All return addresses are on the S stack.  
The T register is used as the code scanner.  
The D & X registers are not preserved between subroutines.  
X is sometimes stuffed with an address by NEXT.  
The JP register is dedicated as the direct page of the interpreter.  
All subroutines must have a unique name.  
Subroutines which behave identically but use different subroutines will be differentiated by a unique suffix. MC code name suffix will identify the name and model of microprocessor (in case the standard is applied to another class or the instruction set of the 6809 is changed).  
Interpreters are allowed.  
The required entry and produced results should be documented, the type declarations of Pascal being a good model.  
A short description of the subroutine should be furnished.  
A subroutine will consist of either machine code obeying the above restrictions or a list of subroutine names. The first name of the list must be JP DSD, the last must be ASD. ASD still has the zero code if possible.  
Local symbols (variables) are globally pushed onto the U stack in memory. An exception is loop indices which are preferably pushed on the S stack if possible.  
When nested local symbols are desired, a frame pointer is used. On entry to the routine the previous frame pointer is pushed on the value stack and the new frame pointer set to the current value of the value stack pointer. On exit the previous value of the frame pointer is restored. The frame pointer resides on a global symbol table entry.  
In interactive applications it is expected that a symbol table will be made in a debugger from memory. It is required that an entry be allocated to hold two semantic values. This allows a variable both a local and a store routine.  
A symbol string in front (any length).  
A next symbol link.  
A one word code describing the type of symbol.  
The second or alternate semantic pointer (if needed).  
The first or normal semantic pointer.  
A value field (any length).

The routines to reference variables in the symbol table varies with each interpreter. The code for is bit loads and stores is:

TC: The normal symbol semantic entry contains a JP to a LD. The alternate entry contains a JP to a ST. The load and store routines must be to the direct page so a jump to them takes but one word (two bytes).  
LDX -2,T  
LD 2,X the symbol value follows the normal entry  
PWS D  
next

LDX -2,T  
PULS D  
STD 4,X the alternate entry precedes the normal entry  
next

TC2: The normal symbol semantic entry contains the address of a LD; the alternate entry the address of a ST.  
LDX -2,T  
PULS D  
next  
LDX -2,T  
PULS D  
STD 4,X  
next

CTC: Following the jump table is the value table with at least two bytes per entry, so that a combined load & store have four bytes of value to store with. The load entry is the address of a LD. This follows two words for the jump table followed by two words for value entries. Each such entry has its own entries in the jump table.  
LDX -2,T  
PULS D  
next  
LDX -2,T  
PULS D  
STD 4,X assuming the ST entry precedes the LD entry  
next

### Some additional interpreters

TC-ITC hybrids, based on some property of a code list address, so that address is used either as the address of MC or the address of the address of MC.

next LDX X  
BPL next X always modifies  
JP X uses sign of address to MC to high memory  
next JP (X,X)  
next LDX 1000 21 & 26 cycles  
OPLD X only changed on ITC  
BPL next uses last value of 256 (ie all MC at a last 256)  
JP (X,X)  
next LDX T  
JP (X,X)

CTC-ITC hybrids, based on some property of the byte code, so that the byte is used either as the index of a jump table or as half of the address of MC.  
Note that variables need not be associated with the jump table.

```

next LDB ,T+ 24 & 29 cycles
BPL data X always changed
LDB ,T+ 24 uses sign of code (all pointers in low memory)
ASLB
JMP 10,11
next LEAF 1,T
LDB ,T+ 24
JMP 1,11

next LDB ,T+ 24 & 33 cycles
BITB ,T+ 24 X always changed
BEO extra uses low bit code (all indirect adrs at even locations)
LDB ,T+ 24
JMP 10,11
next LDB ,T+ 24
JMP 1,11

next LDB ,T+ 32 & 26 cycles
CPB ,T+ 32 X only changed on ITC
BVS next uses limit value, int must be >127
LDB ,T+ 32
ASLB
STB next+3
next LDB ,T+ 32
JMP 1,11

TC infinity, these follow indirect address chain until adr of some particular type found.
next LDB ,T+ 17, 29, 41, ... cycles
BPL extra X always changed
JMP ,X uses sign (all MC in upper half of memory)
next CPB ,X
BPL extra X
next LDB ,X
JMP 1,11
next LDB ,X
JMP 1,11

next LDB ,T+ 21, 33, 45, ... cycles
CPB ,T+ 21 X only changed on indirect
BVS extra uses limit value*756 (all MC above int*256)
LDB ,T+ 21
ASLB
JMP 1,11
next LDB ,T+ 21
JMP 1,11

next LDB ,T+ 21, 33, 45, ... cycles
CPB ,T+ 21 X only changed on indirect
BVS extra uses limit value*756 (all MC above int*256)
LDB ,T+ 21
ASLB
JMP 1,11
next LDB ,T+ 21
JMP 1,11

CTC+ITC infinity, indirect & until code of specific type found. when found, used to index jump table.
next LDB ,T+ 20, 44, 56, 68, ... cycles
BPL extra X only changed on indirect
LEAF ,T+ 20 and left with adr of code
LDB ,T+ 20 uses sign of code (all MC in high memory)
LDB ,T+ 20
JMP 1,11
next LDB ,T+ 20
JMP 1,11

next LDB ,T+ 20, 44, 56, 68, ... cycles
BPL extra X only changed on indirect
LEAF ,T+ 20 and left with adr of code
LDB ,T+ 20 uses sign of code (all MC in high memory)
LDB ,T+ 20
JMP 1,11
next LDB ,T+ 20
JMP 1,11

next LDB ,T+ 20, 44, 56, 68, ... cycles
BPL extra X only changed on indirect
LEAF ,T+ 20 and left with adr of code
LDB ,T+ 20 uses sign of code (all MC in high memory)
LDB ,T+ 20
JMP 1,11
next LDB ,T+ 20
JMP 1,11

The RTS interpreter T and S registers are exchanged. S is the code scanner, hence interrupts are not possible as they would corrupt the code. T is the control stack.

```

Virtual address space interpreters

An address space the size of disk is chosen. Any of the above interpreters may be adapted to the larger address space. However typically a CTC-infinity mode would be chosen which allows variable length address codes.

0xxxxxx addresses 0..63 mean push 0..63 onto the value stack  
 0xxxxxx addresses 64..127 mean do builtin operators 0..127  
 10xxxxxx is followed by 8 more bits of address (16 total)  
 110xxxxxx is followed by 16 more bits of address (32 total)  
 111xxxxxx is followed by 24 more bits of address (128 total)

Finings			
Type	NEXT	OSB	ASD
MC	13(130+151)	0	0
RTS	5(151)	20(151)	9
TC	9	19(151)	7
ITC	1a	18(151)	7
CTC	2a	18(151)	7
TC+ITC	17/20 (using sign bit)	18(151)	7
TC+ITC	21/20 (using limit)	18(151)	7
TC+int	17/29 (using sign bit)	18(151)	7
TC+int	21/33 (using limit)	18(151)	7
CTC+ITC	24/29 (using sign bit)	18(151)	7
CTC+ITC	28/33 (using low bit)	18(151)	7
CTC+ITC	32/28 (using limit)	18(151)	7
CTC+int	20/44 (using sign bit)	18(151)	7
CTC+int	24/48 (using low bit)	18(151)	7
CTC+int	32/45 (using limit)	18(151)	7

Note that the RTS and TC interpreters are faster than MC. This means that if your program consists of a sequence of subroutine calls (nowdays considered good programming practice), then either RTS or TC will be faster than the MC to the subroutine and the associated RTS back. In addition the code will be more compact. This leads me to conclude that only the lowest level subroutines should be coded in machine code (MC). All other should be coded in interpreter code.

```

% Name: LARGPRIM.TXT
% Purpose: List Mersenne primes
% and primes of form 3*2^n-1 & 9*2^n-1
% and primes of form 3*2^n+1, 5*2^n+1, 7*2^n+1 & 9*2^n+1
% and the known perfect numbers
% which for Mersenne prime 2^n-1
% equals (2^n-1)*2^(n-1)

% Proper set of constants selected by commenting out the others

% Numbers listed without CR-LF, so CRT must have wrap-around to
% see complete numbers. This program takes 55 minutes! Normally
% D.CMD used to save results. File generated will be up to 160
% blocks long. Perfect numbers take 8.1 hours and 331 blocks.
% (1Mhz 6809 system)

% Flex adrs
OUTCH EQU $CD0F
PCRLF EQU $CD24
INCH EQU $CD0C
OUTDEC EQU $CD39 B=0, adr of num in X
FLEX EQU $CD03

% Interpreter
NEXT MACRO .
JMP [Y++]
ENMD

START LOU $PRMS
LDY $PROG
NEXT

% Code for primes
$PROG FDB CRLF,CLEAR,POOL,MISC
% FDB POOL,DBL,LOOP
% FDB HEADNG,CRLF,DEC,PRNUM,CRLF,CRLF,LOOP,FLEX

% Code for Perfect numbers
PROG FDB CRLF,POOL,MISC2
FDB CLEAR,POOL,DBL,LOOP,DEC form 2^n-1
FDB POOL,DBL,LOOP and double n-1 times
FDB HEADNG,CRLF,PRNUM,CRLF,CRLF,LOOP,FLEX

% Routines

% Set number to 1
CLEAR LDX $NUM
CLL CLR ,X+
CMPX $NUMEND-1
BNE CLL
LDA #1
% LDA #3
% LDA #9
STA ,X
NEXT
% Double number (base 10)
% Optimized by unwinding & expanding length num
DBL LDX $NUMEND
PSHS Y
CLC
DBLL LDA ,X
ADCA ,X
DAA
STA ,X
LDA ,X do 4 digits per pass
ADCA ,X
DAA
STA ,X
DBLA LEAY 2-$NUMEND,X offset modified as size increases
BNE DBL
BCC DBLB expand size if carry
INC ,X

```



```

LDB DBLA+2
ADD #2
STD DBLA+2
DBLB PULS Y
NEXT

; Subtract one from number (with no borrow)
DEC DEC NUMEND-1 for k(2n-1) primes
DEC INC NUMEND-1 for k(2n+1) primes
NEXT

; Print number (2 bcd digits per byte)
PATNUM LD# NUM
PRTLA TST ,X+
BEQ PRTLA
LDA ,X
ANDA #0F0
BEQ PRTB
PRTL LDA ,X
LSRA
LSRA
LSRA
LSRA
ADDA #00
JSR OUTCH
PRTB LDA ,X+
ANDA #0F
ADDA #00
JSR OUTCH
CMPX NUMEND
BNE PRTL
INC NUMEND-1 for k(2n-1) primes
DEC NUMEND-1 for k(2n+1) primes
NEXT

; Enter count-down loop
POOL PULU D
PSHS D,Y
NEXT

; Exit test on count-down loop
LOOP PULS X
LEAX -1,X
PSHS X
BNE LOOPN
LOOPA LEAS 4,S
NEXT
LOOPN LDY 2,S
NEXT

; Prepare loop count (0 times to double number)
; Value stk= last prime, this prime,...-> difference, this prime,...
MISC LDD 2,U
SUBD ,U
STD ,U
NEXT

; Prepare loop counts for perfect numbers
; Value stk= last prime, this prime,...-> this prime, this prime-1,
; this prime,...
MISC2 LDD 2,U
PSHU D
SUBD #1
STD 2,U
NEXT

; Print heading
; Value stk= this prime, ...
HEADING LD# MSG1
JSR PSTRNG
TFR U,X
CLRD
JSR OUTDEC

```

```

LD# MSG2
JSR PSTRNG
NEXT

; Header for primes, select one MSG1 and one MSG2
MSG1 FCC ' 2 11 ',0
MSG1 FCC ' 3 11 11 ',0
MSG1 FCC ' 5 11 11 11 ',0
MSG1 FCC ' 7 11 11 11 11 ',0
MSG1 FCC ' 9 11 11 11 11 11 ',0
MSG1 FCC 'Perfect number for n=',0
MSG2 FCC ' - 1 =',0
MSG2 FCC ' + 1 =',0
MSG2 FCC ', (2n-1)(2n+1) =',0

; Print string, not threaded
PSTL JSR OUTCH
PSTRNG LDA ,X+
BNE PSTL
RTS

; Print CR-LF
CRLF JSR PCRLF
NEXT

; These constants are initialization for value stack, which is # of
; exponents, zero & the exponents.

; Primes of the form K & 2 n - 1
PRMS FDB 26 # of primes, K=1, Mersenne primes
FDB 0,2,3,5,7,13,17,19,31,61,89,107,127,521,607,1279,2203,2281
FDB 3217,4253,4423,9689,9941,11213,19937,21701,44497
1PRMS FDB 31 K=3
FDB 0,1,2,3,4,6,7,11,10,34,38,43,55,64,76,94,103,143,206,216,306
FDB 324,391,450,470,827,1274,3276,4204,5134,7559,12676
1PRMS FDB 20 K=9
FDB 0,1,3,7,13,15,21,43,63,99,109,159,211,309,343,415,469,701
FDB 871,939,1551,3115,3349,5509,5815,5893,7939,8007,11547

; Primes of form K & 2 n + 1
1PRMS FDB 5 K=1
FDB 0,1,2,4,8,16
1PRMS FDB 24 K=3
FDB 0,1,2,5,6,8,12,10,30,36,41,66,109,201,209,276,353,400,430
FDB 534,2200,2016,3160,3109,3912
1PRMS FDB 16 K=5
FDB 0,1,3,5,7,13,15,25,39,55,75,85,127,1947,3313,4607,5947
1PRMS FDB 22 K=7
FDB 0,2,4,6,14,20,26,50,52,92,120,174,100,190,290,320,390,432
FDB 616,830,1004,2256,6614
1PRMS FDB 31 K=9
FDB 0,1,2,3,6,7,11,14,17,33,42,43,63,65,67,81,134,162,206,211
FDB 366,663,702,1305,1411,1494,2297,2026,3230,3354,3417,3690

SIZE EQU 13396 must be multiple of 2, see DBL
NUM RMB SIZE space for number in decimal, ^3010304449701
NUMEND EQU # 1+other end of number

; Primes taken from various issues of Mathematics of Computation
; Also see Recreational Mathematics by Deiler
END START

```

Perfect number for  $n=2$ ,  $(2^{n-1}) \cdot 2^n (n-1) = 6$

Perfect number for  $n=3$ ,  $(2^{n-1}) \cdot 2^n (n-1) = 28$

Perfect number for  $n=5$ ,  $(2^{n-1}) \cdot 2^n (n-1) = 496$

Perfect number for  $n=7$ ,  $(2^{n-1}) \cdot 2^n (n-1) = 8128$

[illegible]

Perfect number for  $n=17$ ,  $(2^{17}-1)*2^{16} = 8589869056$

Perfect number for  $n=19$ ,  $(2^{19}-1)*2^{18}*(19-1) = 137438691328$

Perfect number for  $n=31$ ,  $(2^{n-1}) \cdot 2^n(n-1) = 2305843008139952128$

Perfect number for  $n=61$ ,  $(2^{61}-1)*2^{60} = 2658455991569831744654692615953842176$

Perfect number for  $n=89$ ,  $(2^{89}-1) \cdot 2^{89} \cdot (n-1) =$   
 191561942608236107294793378084303638130997321548169216

Perfect number for  $n=107$ ,  $(2^{107}-1) \cdot 2^{106} =$   
 13164036458569648337239753460458722910223472318386943  
 117783728128

Perfect number for  $n=127$ ,  $(2^{**}n-1)*2^{**}(n-1) =$   
 14474011154664524427946373126085988481573677491474835  
 889066354349131199152128

Perfect number for  $n=521$ ,  $(2^{521}-1)*2^{521} =$   
 2356272345726734706578954899670990498847754785839260  
 0710143027597506337283178622239730365539602600513602  
 555664625032701705528925780432155433824984287715242  
 70103944969186640286445341280338314397902368386240331  
 71435921256643219703101720713163527487298747400647801  
 939587165936401087419375649057918549492160555646976

Perfect number for  $n=607$ ,  $(2^{607}-1) \times 2^{606} \times (n-1) =$   
 14105378370671206906320795808606318988148674351471566  
 738838675999548677426523801141041933290376902515619  
 50568709829327164087724366370087116731268195313652487  
 406552439805877279620729794466732951666582288469268077  
 8665287018892086787945147836456931392206037069506473  
 60735723786951764730552668262532848863873150792794324  
 463835300053138294602965714336806557075957328128

[illegible][illegible]



CHX80	NO	DA	YR	CHK	ACCOUNT	AMOUNT	DEPOSIT	BALANCE
00	00	00	0000		BALANCE FORWARD	.00	359.62	359.62
01	01	00	0000			.00	350.00	909.62
01	01	00	0231		CASH	150.00	.00	759.62
01	01	00	0232		GASOLINE	34.60	.00	725.02
01	01	00	0233		WATER	27.00	.00	697.22
01	01	00	0234		TELEPHONE	32.45	.00	664.77
01	01	00	0235		HEATING	112.50	.00	552.27
01	10	00	0236		VISA	100.00	.00	452.27
01	15	00	0237		CASH	125.00	.00	327.27
02	01	00	0000			.00	350.00	877.27
02	01	00	0238		GASOLINE	44.50	.00	832.77
02	01	00	0239		HEATING	112.50	.00	720.27
02	01	00	0240		TELEPHONE	26.40	.00	693.87
02	01	00	0241		CASH	150.00	.00	543.87
02	10	00	0242		VISA	100.00	.00	443.87
02	10	00	0243		INSURANCE-LIFE	45.65	.00	398.22
02	15	00	0244		CASH	190.00	.00	298.22
--(etc)--								

Once the checking file is established, we can do some useful manipulation using the GENER program. For example, I have found it useful to see the monthly expenses in a matrix format. This is easily accomplished with the following control statements

```
FILE CHX80
PRINT ACCOUNT
PRINT AMOUNT FOR MO = 01 AND 02 AND 03 AND 04 AND 05 AND 06...
SUM BY ACCOUNT EXC IF CHK0 > 0000
TOTAL, TABS 0, END
```

For purposes here, the matrix is limited to six months by the column width. Users with a 132 column printer will find that all 12 months will just fit with the field sizes given (24 columns for ACCOUNT plus 12 x 9 for AMOUNT = 132). The resulting output appears as follows.

GENER	AMOUNT					
ACCOUNT	01	02	03	04	05	06
CASH	275.00	250.00	275.00	300.00	500.00	250.00
DR. HYDE	.00	.00	35.00	.00	.00	.00
DR. JEDYK	.00	.00	.00	35.00	.00	.00
GASOLINE	34.60	44.50	39.00	42.50	135.00	29.45
HEATING	112.50	112.50	112.50	112.50	112.50	112.50
INSURANCE-CAR	.00	.00	112.50	.00	.00	112.50
INSURANCE-LIFE	.00	45.65	.00	.00	45.65	.00
TELEPHONE	32.45	26.40	39.00	27.85	23.44	22.90
VISA	100.00	100.00	89.44	.00	37.31	.00
WATER	27.00	.00	.00	27.00	.00	.00
	582.35	579.05	744.04	545.65	854.70	527.35

We could get "fancy" and add a title and perhaps rename the columns as "JAN", "FEB"... with a second execution. Another possibility is to define a "class" code into our checking file for "MED", "AUTO" or other indicators. This would allow summary of these categories into a matrix and would be useful at tax time. Note that entry of the "SUM BY" fields must be consistent in either case. If space permits, line totals can be also generated using CALC statements. Many other possibilities exist and I will leave them to the imagination of the reader.

With the mailing list discussed last month, we now have two applications for our DMS system. By now the reader should begin to appreciate the flexibility and ease of use that a DMS can offer. If we employed

DMS2/VM for both applications, the cost is only \$50. each and we have only scratched the surface as to the applicability. In future months, I hope to examine some more applications, thereby showing how the cost per application can be further reduced.

## COLOR GRAPHICS

HI - RES ANIMATED  
COLOR GRAPHICS

PART II - The ARCADE-50 from TERMINUS DESIGN

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A few months ago, a gaping hole in the SS-50's repertoire of boards was filled when TERMINUS DESIGN introduced the ARCADE-50. PART I of this article discussed a major component of the ARCADE-50 -- the TMS9918A Video Display Generator. But that was only part of the story. The complete ARCADE-50 can do much more. It was designed to generate animated color graphics, produce a wide range of music and sound effects, and to provide a sensible human interface -- and it succeeds amazingly well!

The obvious application for this board is, of course, the generation of real-time arcade games, complete with joysticks, fire buttons, and sound effects. However, do not fall into the trap of limiting your imagination to this one specialized area. The addition of graphics to almost any computer application will inevitably enhance its clarity and ease of use. The sound generating capabilities should not be casually dismissed either. Those interested in music composition or synthesis should be able to do wonders with the nine voices and many tonal effects that are available. In fact, your imagination will be your only limitation.

### THE HARDWARE

The board itself takes up one SS-50 slot and comes fully assembled and tested. Although the added frills of solder mask and silk screening were foregone, the board, layout and assembly show a better than average level of professionalism. Gold connectors are an option which I decided to omit.

A TMS-9918A chip provides the video capabilities, and three AY-3-8910 Sound Generator chips weave the aural effects. In addition, the board supports several necessary pieces of hardware. Inputs for up to four X-Y proportional joysticks (with pushbuttons) plus eight bits of uncommitted parallel I/O are provided. An audio output hooks directly to an 8 ohm speaker and a composite video output will directly connect to a color monitor -- or, optionally, go to an RF modulator for input into a color TV set. An external video input (from a video camera or VTR) is also available for those who desire to work with this option. Conveniently, an on-board sync separator circuit has also been provided. Access to these inputs and outputs is via three DIP headers.

All the necessary external hardware can be obtained directly from TERMINUS DESIGN at added, but reasonable, cost -- or the user may provide them himself. External cables and connectors come unassembled from TERMINUS DESIGN to allow custom fit for the individual enclosure.



I received the cable and connector set, two Radio Shack joysticks, and a SUP-R-MOD RF modulator. Although the RF modulator is off-board and somewhat inconvenient to mount, I am sold on this arrangement. I have three other color video boards with various RF modulator arrangements, but I consistently get the best picture with the SUP-R-MOD. A little experimenting directly attributed this to three factors:

- 1). Keeping the modulator and digital circuits physically separate,
- 2). Effective shielding, and
- 3). Using UHF channel 33 instead of VHF channel 3.

It took a couple of evenings work to drill some holes, mount and hook up the connectors, and find a convenient place to set the color TV. The board itself offers two jumper options: The vertical retrace signal can go to either FIRQ or IRQ, and the audio output can be either stereo or monaural (there are two on-board one watt audio amplifiers).

A DIP switch allows addressing the board to any 256 byte block. This is somewhat distressing, as it actually uses only six bytes of memory space. Sadly, I have found that all manufacturers tend to cut corners, to some degree, in this area. With today's overworked memory map, this practice must be corrected in any future designs.

#### THE MANUAL

Most budding manufacturers soon find out, much to their chagrin, that writing an effective instruction manual is no trivial task. Most initial efforts leave a bit to be desired, and the ARCADE-50 manual did not prove to be the exception. Actually, every needed piece of information is there, but one cardinal rule was violated. It is not totally and unequivocally obvious, on first reading, just exactly where to start and what to do with all the information given.

After a couple of readings (the manual is well printed and in very readable type size) things do start falling into place. The first few pages give system requirements, how to hook up the connectors, and initial checkout. Next follows a series of assembly language drivers to handle the various primitive functions of the devices. It will take some study to fully understand their proper use.

Also included is the TMS-9918A VOP programmer's manual and the AY-3-8910 Programmable Sound Generator manual. This encompasses about 60 pages of extremely useful information. It turns out that these have been my primary reference sources while writing programs for the ARCADE-50.

#### BRINGING IT ON-LINE

Before plugging the board in, a change or two may be in order. First, I found that C11, the video output capacitor, is not only unnecessary, but actually detrimental to video reception. It should be removed and a jumper put in its place. Also, VMA was not connected on some earlier boards. This evidently only poses problems on 6800 systems. TERMINUS DESIGN has now corrected this situation.

After making all necessary connections and plugging everything in, you are now ready for the smoke test. If you are running a standard FLEX09 system (whatever that means) your problems are almost over. A FLEX09 demo disk is included that has several programs which exercise and check out various board functions. Unfortunately, most of these fall into the class of "Not-Ready-For-Prime-Time" programs. Instructions for use are too casually mentioned in

the manual and the programs themselves are not really production-ready. Some of them prompt for a board address, others require editing. Some have ill-defined and unexpected inputs and outputs. I finally ended up listing them all, deciding what they were supposed to do, making changes, and then running them. Terminus Design now recognizes these problems and is in the process of preparing a more meaningful demo disk.

There is one program on the demo disk that does deserve praiseworthy mention. It is a very good PAC-MAN type game, complete with sound effects, called RATMAZE. It operates under Terminus Design's proprietary real-time operating system (object included) and requires IRQ and a serial terminal. The only problem encountered was that the program expects the console ACIA to be addressed at \$E004. My system is (to put it mildly!) not exactly standard anything. I had to change seven absolute addresses in RATMAZE that referenced the ACIA. For those with a similar problem, the seven addresses are:

\$0839	\$0970	\$09B2
\$093E	\$097F	\$0958
\$0941		

Once the RATMAZE program is running, it is singularly impressive. The smooth animation, sound, and color only served to whet my appetite to create bigger and better programs. I was further impressed by the fact that all this was now happening on MY computer! Even my children have started begging to learn about the computer (sometimes I wonder if this is really a step forward). Now that everything is running and the capabilities of the ARCADE-50 actually sink in, all problems have dwindled into perspective -- minor inconveniences only!

#### SOUND GENERATING CAPABILITIES

It is sometimes difficult to decide whether the ARCADE-50 is primarily a graphics board or primarily a sound generating board. This paradox is created by the presence of three General Instruments AY-3-8910 Programmable Sound Generator (PSG) chips. With two LM386 audio amplifiers on-board, it is only necessary to connect the appropriate speakers to obtain musical or special sound effects.

The ARCADE-50 offers a stereo or monaural option, selected by jumpers. In monaural, the outputs of all three PSG's are mixed into one amplifier. For stereo, two PSG's are mixed into one amplifier, while the other amplifier is dedicated to the third PSG. Stereo, of course, will require the addition of two speakers.

For those unfamiliar with the AY-3-8910, it is a register oriented sound generator. That is, all control commands are given to the PSG by writing to one of its sixteen address registers. Once the registers are loaded, the PSG takes over to generate and sustain the programmed sound. The system processor is then free to perform other tasks.

Each PSG contains three separate tone generators which have a range of subaudio to about 75,000 cycles. Each channel also has its own amplitude register, programmable to one of sixteen levels. I am estimating a dynamic range of the total circuit to be only about 35 db. -- not the best, but certainly far from being unacceptable.

There are two other special effects items on each PSG that have a global effect on all three tone channels. One is a pseudo-random noise generator that can be mixed in varying amounts with the tone generators. This is very useful in creating effects

such as explosions, gun shots, snare drums, and steam locomotives. The other necessary tool available is the Envelope Generator. This allows the programmer to have control over the attack, sustain, and decay of the particular sound.

Add three such PSG's onto one board and you have some very exciting potential. For those interested in musical composition or synthesis, the nine separate tone channels offer a wide range of possibilities. I have heard some rather impressive music generated on the ATARI 800, which has only four tone channels. I can only imagine what some creative individuals will do with nine channels available!

#### SOFTWARE AVAILABLE

Inevitably, the more complex and flexible a board is, the more heavily it relies on software. It would be absurd to expect a small manufacturer to have a full catalogue of applications programs instantly available. Instead Terminus Design chose, and correctly so, to concentrate their efforts towards providing the user with adequate tools to generate quality applications programs.

The tried-and-true route is to provide a high level language -- usually BASIC -- with a set of built in commands for graphics and sound manipulations. Terminus Design went one step further and secured a BASIC Compiler! FBASIC, as Terminus Design calls it, is in reality an enhancement of Microware's A-BASIC compiler. About 31 commands were added to A-BASIC for controlling the graphics and sound functions of the ARCADE-50. FBASIC also retains all of the original A-BASIC functions, as well as allowing disk I/O through FLEX.

While FBASIC is a minimal implementation of Basic, with only 16 bit integer math and no scientific functions, it makes ultimate sense for an application like this. The primary advantage is program execution speed. A Basic Interpreter is almost invariably too slow to produce a meaningful real-time graphics display. Also, arcade games seldom compute any complex functions "on the fly". Thus, the FBASIC math package is more than adequate for most applications. Finally, as it is a high level language, program development time is drastically reduced.

It is totally beyond the scope of this article to discuss all the nuances of FBASIC -- I will reserve that for future topics. I will say that, even though it has a few problems, I have found it to be an invaluable development tool. I strongly recommend this option to anyone purchasing the ARCADE-50.

Presently, the only alternative to FBASIC is to use some assembly language drivers. A minimal set of drivers is included with the ARCADE-50 documentation. I have written a more extensive driver set that handles all functions in all modes and occupies a little less than 2K of memory. These could be used with assembly language applications programs or patched into an Interpreted Basic via USER calls. The source code is in the hands of Terminus Design and may be available through them.

Terminus Design is also setting up a user's group for applications software. I have seen several of the initial efforts and they all look promising. Included is the already-mentioned RATMAZE game, an OTHELLO game, a SPRITE CREATOR/EDITOR program, and a program that allows drawing figures with the joystick and creating three dimensional images. Consult Terminus Design for availability.

#### CONCLUSION

The difficult part about writing a review of a board like the ARCADE-50 is answering the question -- "Where does one stop?". With boards of this potential and versatility there will always be just one more function to describe or just one more point to clear up. Perhaps that is what is so interesting about graphics and sound -- the continual challenge. After all, what can be said about a memory board, an I/O port, or a disk controller? Once operational, they cease to be entities in themselves, and simply blend invisibly into the system.

Ah, but boards like the ARCADE-50 are an entirely different story! They continually make their presence known, constantly begging for bigger and better programs. They will continually test your ability and ingenuity as a programmer and, hopefully, inspire you to rise to the occasion.

## UNIFLEX FORTRAN UTILITIES

#### STRING-HANDLING UTILITIES FOR UNIFLEX FORTRAN

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For computer-assisted instruction, the application in which I work, you need to be able to easily deal with character strings. Of all the languages you can pick from, FORTRAN is probably the most unlikely choice for such an application, but for several years, we were time-sharing on a computer on which FORTRAN was by far the most flexible language. That computer had a large library containing just about every utility you could ever want. Over the years, I wrote dozens and dozens of FORTRAN programs, all using these utility subs. When we got our own Southwest Tech 8/09 computer and UNIFLEX FORTRAN, it was clear that the first thing I had to do was to write utilities just like the ones on the old computer. Once that was done, the conversion of the application programs was a fairly straightforward chore.

This paper describes 13 of those utility subs which I think would be useful to anyone working with this version of FORTRAN. Most of the subs are written in source code for the relocating assembler, and rest are in FORTRAN. The general procedure for incorporating machine-language utilities into UNIFLEX FORTRAN was described in an earlier paper of mine published in this Journal.

UNIFLEX is, of course, a multi-user operating system of Technical Systems Consultants, and a trademark of theirs. TSC released FORTRAN 77 for UNIFLEX early in 1982. To use FORTRAN 77 you also need the relocating assembler and the linkage loader.

In FORTRAN 77 one can declare a character string of any size. For example, the declaration:

```
character*40 LINE
```

defines a string called "LINE" which contains a maximum of 40 characters. I am going to call this type of variable a character "string" in contrast to a character "array". An example of a character array declaration is the following:

```
character*81 TEXT(40)
```

Most of the routines below deal with strings rather than arrays, but two conversion routines are included: STOA converts a string into an array, and ATOS converts an array into a string.

User input is usually done as follows:

```
character*40 LINE
```

```
.....
```

```
read(3,100) LINE  
100 format(a40)
```

The read statement will accept just about anything the user types. In CAI you must not force the user to format his or her input in any particular way! Once the program has the input string, it can proceed to analyze it to see if it makes sense in the current situation. For example, if the user was supposed to input an integer value, the program can use the MURGET function to extract that value.

The string "LINE" in the read statement above will receive the first 40 characters that the user types. If the user types less than 40 characters, the string is padded on

the right with spaces. NO END OF STRING TOKEN IS INSERTED! Therefore we will deal with strings in this format. No end of string token will be used, and strings will be padded on the right with spaces.

The source files are given in the two listings. The filename of Listing 1 is 'packase1'. This is a source file for the relocating assembler. The Fortran source code in Listing 2 should be named 'packase2.f'. The following commands will create a library called 'packase' which contains all of the subprograms:

```
releasb packase1
f77 packase2.f &c
lib-gen n=/lib/packase u=packase1.o u=packase2.o
```

Once this library is created in this manner, it can be accessed by any user via the 'link-edit' command as described in Chapter 2 of the manual entitled 'UniFLEX Linkage Editor'.

What follows is a description of each of the routines. Copies of this may be distributed to all of your FORTRAN users.

```
*****
Strings-Handling Utilities
*****
```

For an explanation of the use of libraries, read Chapter 2 or 'UniFLEX Linkage Editor'.

This information sheet describes the routines in the library, '/lib/libbxm'. These routines deal mainly with character strings. Since strings can be used as the filename in an OPEN statement, these routines help to manipulate filenames, but their presence also makes it practical to use strings instead of character arrays.

All Fortran programs require the library: '/lib/F77.runlib'.  
Definitions:

A character STRING is a character variable of size greater than one. For example, the following declaration forms a character string of size 40:  
character\*40 LINE

The SIZE of a string is the size specified in its declaration.

The LENGTH of a string is the number of characters up to the last non-blank character. Note that there is no end-of-string marker in filenames or any other strings.

A character ARRAY is an array of characters or character strings. Note the difference in the following array declaration from the previous string declaration:  
character(40) TEXT(40)

Both LINE and TEXT contain exactly 40 characters, but they are stored differently in memory.

The POSITION of a character within a string is 1 for the first character.

#### 1. subroutine CFILL(KHAR,STR,IPOS,NCH)

Fills string STR with character KHAR.  
IPOS = beginning position within STR where first character will be placed.  
NCH = number of characters to be replaced.

For example, to fill an entire string with blanks:  
character\*50 line  
call cfill(' ',line,1,50)

#### 2. subroutine CMOVE(STR1,IPOS1,STR2,IPOS2,NCH)

Copies characters from STR1 to STR2.  
STR1 = character string to be copied FROM.  
IPOS1 = position within STR1 of the first character to be copied.  
STR2 = character string to receive the characters.  
IPOS2 = position within STR2 where first character will be placed.  
NCH = number of characters to be copied.

#### 3. integer function NACS(LINE,LSIZE)

Finds the length of a string. Actually, what it finds is the last non-blank character in the string.  
LINE = character string.  
LSIZE = size of the string.

For example, to find the number of characters typed by user:

```
character*40 line
read(5,1) line
1 format(a40)
len=nacs(line,40)
```

Note: LSIZE must equal the declared string size.

#### 4. subroutine CONCAT(STR1,L1,STR2,L2,STR3,L3)

Lets STR3 = concatenation of STR1 and STR2.  
L1 = declared size of STR1.  
L2 = declared size of STR2.  
L3 = declared size of STR3.

1. finds length of STR1 (using NACS)
2. moves that many characters to STR3
3. finds length of STR2

4. moves that many characters to STR3
5. fills the rest of STR3 with blanks

If the length of STR1 + length of STR2 is greater than the size of STR3, then appropriate truncation will occur.

#### 5. subroutine LPARSE(LINE,LSIZE,KPTR,IBEO,JSIZE)

Parses fields within a string. Can be used iteratively to parse all the fields in the string. Fields are separated by blanks or a single comma. Extra blanks are ignored.

LINE = character string.  
LSIZE = declared size of LINE.  
KPTR = sliding pointer. This must be a VARIABLE, never a constant! It should be initialized to the first character to examine. The routine increments this variable as it processes the string and returns with KPTR pointing to the first character after the last character in the field.  
IBEO = integer VARIABLE! Returns as the position of the first character of the next field, or as zero if there were no more fields in the string.  
JSIZE = integer VARIABLE! Returns as the number of characters in the field which starts at IBEO, or as zero if the field was null (i.e. ',,') or not found.

#### 6. integer function MATCH(LINE,LSIZE,STR,NCH)

Determines whether STR is a substring of LINE.  
LINE = the longer character string.  
LSIZE = declared size of LINE.  
STR = the shorter character string.  
NCH = number of characters of STR to check.

MATCH will return with a value of zero if STR does not match any part of LINE. If it does match, the value is the position of the first character of LINE that matches the first character of STR.

#### 7. subroutine LEFT(STR1,STR2,LSIZE,NCH)

Lets STR2 = the left NCH characters of STR1  
LSIZE = declared size of STR2

The NCH left-most characters of STR1 are transferred to STR2 and the remainder of STR2 is filled with blanks. If NCH is greater than LSIZE, then STR2 causes the left-most LSIZE characters of STR1.  
If NCH = 0, then STR2 will be filled with blanks.

#### 8. subroutine MID(STR1,IPOS1,LSIZE1,STR2,LSIZE2,NCH)

Lets STR2 = a portion of STR1.  
IPOS1 = position within STR1 of where the new string starts.  
LSIZE1 = declared size of STR1.  
LSIZE2 = declared size of STR2.  
STR2 includes NCH characters from STR1. The remainder of STR2 is filled with blanks.

#### 9. integer function ICOMP(STR1,IPOS1,STR2,IPOS2,NCH)

Compares two strings.  
IPOS1 = position within STR1 of first character of the field.  
IPOS2 = position within STR2 of first character of the field.  
NCH is the number of characters to compare.  
If the STR1 field is identical to the STR2 field then ICOMP = 0.  
If the STR1 field precedes the STR2 field in alphabetical order, then ICOMP = 1.  
If the STR1 field follows the STR2 field in alphabetical order, then ICOMP = -1.

#### 10. integer function NUMDET(LINE,IPTR,NCH)

Extracts a decimal integer from a string.  
LINE = character string containing an integer represented by decimal digits.  
IPTR = sliding pointer. This must be a VARIABLE, never a constant! It should be initialized to the position within LINE of the first character to examine. It will be incremented to one place beyond the last digit of the number.  
NCH = Maximum number of characters to examine before stopping.  
NUMDET is assigned the value of the integer found or zero if no digits were found.

Leading spaces or commas are ignored. The scan terminates on any of the following conditions: (1) NCH characters have been examined; (2) a character is encountered which is not a digit, space, or comma; or (3) a space or comma is encountered after a group of digits.

#### 11. subroutine STOA(LINE,ARRAY,LSIZE)

Converts a character string to a character array.  
LINE = character string.  
ARRAY = character array.  
LSIZE = number of characters to convert.

For example:  
character\*50 LINE  
character\*1 ARRAY(50)  
\*  
\*  
\*  
call stoa(LINE,ARRAY,50)

Both LINE and ARRAY contain exactly 50 characters, but they are stored differently in memory.

## 12. subroutine ATOS(LINE,ARRAY,LSIZE)

Converts a character array to a character string.  
LINE = character string.  
ARRAY = character array.  
LSIZE = number of characters to convert.

This is the opposite of STOA.

## 13. subroutine CHR(CHAR,INT)

Lets CHAR = the character whose ASCII value is INT.

This is the opposite of the intrinsic function: ICHAR.

## \* LISTING 1: Source file for relocating assembler

```
name _packdel
* * * CFILL * * *
*
* subroutine cfill(KMAR,STR,IPDS,MCH)
*
* global _cfill
* text
* _cfill leax 2,% points to argument list
* pshs d,u
*
* Find STR position
*
* ldd 2,% STR base address
* addd [4,%] IPDS
* subd #1
* tfr d,u u points to STR character
*
* Fill the string
*
* lds [%] fill character, KMAR
* ldx [%] % is the character counter
* cmov #0 MCH must be positive
* bne cfill2 if not, then leave STR alone
* cfill1 sts %ut
* leax -1,% decrement the counter
* bne cfill1 next character
*
* Clean up the stack & return
*
* cfill2 puls d,u,%
*
* * * CMOVE * * *
*
* subroutine CMOVE(STR1,IPDS1,STR2,IPDS2,MCH)
*
* global _cmove
* text
* _cmove leax 2,% points to argument list
* pshs d,v,u
*
* Find STR2 position
*
* ldd 4,% STR2 address
* addd [6,%] IPDS2
* subd #1
* tfr d,u u points to STR2 character
*
* Find STR1 position
*
* ldd %, STR1 address
* addd [2,%] IPDS1
* subd #1
* tfr d,v v points to STR1 character
*
* Move characters
*
* ldx [B,%] % is the byte counter
* cmovl leax -1,% decrement counter
* cmov #0 set M flag
* bne cmove2 done well?
* lds %ut
* sts %ut
* bra cmovl next character
*
* Clean up the stack & return
*
* cmove2 puls d,v,u,%
*
* * * NACS * * *
*
* Number of Actual Characters in String
*
* integer function NACS(LINE,LSIZE)
*
* global _nacs
* text
* _nacs leax 2,% points to argument list
* pshs d,v,u save registers
*
* Set up pointers and counters
*
* ldd %, LINE address
* addd [2,%] LSIZE
* tfr d,u u points to LINE character
* ldx [2,%] v is the counter
*
* Scan LINE in reverse order
*
* Look for first non-blank character
*
* nacs1 lds %u
```

```
case #20 blank
bne nacs2
leax -1,% decrement counter
bne nacs1

* Leave result in the stack & return
*
nacs2 sty 4,%
puls d,v,u,%
*
* * * LPARSE * * *
*
* subroutine LPARSE(LINE,LSIZE,KPTR,IBEG,JSIZE)
*
* global _lparse
* text
* _lparse leax 2,% points to argument list
* pshs d,v,u
*
* Initialize IBEG and JSIZE
*
* ldd #0
* std [4,%] IBEG
* std [8,%] JSIZE
*
* Find LINE position
*
* ldd %, LINE address
* addd [4,%] KPTR
* subd #1
* tfr d,u u points to LINE character
*
* Find number of characters remaining
*
* ldd [2,%] LSIZE, total number of chrs
* addd #1
* subd [4,%] KPTR
* bis %r06 make sure it is positive!
* tfr d,v v is the number of chrs left
*
* Look for next non-blank character
*
* parse1 lds %ut set next char
* case #20 blank
* bne parse2
* case #2c comma
* bne parse3
*
* The character is a blank or a comma
*
* parse2 leax -1,% decrement char counter
* bne parse1 end of LINE?
*
* End of Line, KPTR=LSIZE+1
*
* parse7 ldd [2,%]
* addd #1
* std [4,%]
* bra parse6
*
* Save IBEG value
*
* parse3 tfr u,d
* subd %,
* std [6,%] IBEG
*
* Count chrs until next space or comma
*
* parse4 ldd [8,%] increment JSIZE
* addd #1
* std [8,%]
* leax -1,% decrement char counter
* bne parse7 end of line?
* lds %ut next character
* case #20 space
* bne parse5
* case #2c comma
* bne parse4
*
* End of field, save KPTR
*
* parse5 tfr u,d
* subd %,
* std [4,%] KPTR
*
* Clean up the stack & return
*
* parse6 puls d,v,u,%
*
* * * MATCH * * *
*
* integer function MATCH(LINE,LSIZE,STR,MCH)
*
* global _match
* text
* _match leax 2,% points to argument list
* pshs d,v,u
*
* Initialize MATCH
*
* ldd #0
* std [B,%] MATCH
*
* MATCH + MCH > LSIZE ?
*
* match1 ldd B,% MATCH
* addd [6,%] MCH
* cmov [2,%] LSIZE
* bhi match3 if so, abort
*
* No, keep going
```





# THE COMPLETE BUSINESS SYSTEM

## + Multiuser + Highly Expandable + Cost Effective

### S+ THE CONCEPT

The S+ system is a modular computer system in which all portions of the hardware and software are designed to work together in the most efficient way possible. An S+ single user system with floppy disk storage is a competitive and cost effective entry level system. Unlike most other small computers being sold as "personal", or "small business" machines, the S+ system may be expanded to maximum capabilities using this same hardware and software. You cannot end up with a DEAD END system that cannot be expanded and whose software is not compatible with larger machines. A basic S+ system may be expanded to thirty-two users, a megabyte of main memory and hundreds of megabytes of hard disk storage by simply plugging in, or connecting the desired upgrade equipment.

### TOTAL DESIGN—Hardware and Software

The S+ system is an integrated hardware and software design. The two complement and enhance each other in this system. The UniFLEX® operating

system used in the S+ systems is patterned after the Bell Laboratories UNIX® operating system, one of the most admired and widely used operating systems in the world. Instead of being an afterthought, the software is part of the design of the S+ system. You can be sure that with this approach that all parts of the computer operate with maximum efficiency and cost effectiveness.

### THE CENTRAL PROCESSOR

The basic S+ system is configured with 256K bytes of memory and can be expanded to more than 1 million bytes. An efficient and fast hardware memory management system is used to allocate the available memory among the users on a dynamic basis. As little as 8K bytes, or the entire memory—if needed—can be used by any individual user. This makes it possible to run very large programs on the system, but it also uses no more memory than necessary for a particular job. The increase in cost effectiveness of this system over crude and outdated bank switching arrangements is dramatic.

The central processor runs in both user and supervisor states. It can detect and reject a defective user program. It is impossible for a user program to go bad and stop the entire system, as can happen quite easily in less sophisticated systems.

Task switching is accomplished by use of a multiple map RAM memory, with sixty-four individual task maps. Each task can access from 4 to 64 K-bytes of memory. Multiple tasks may be used in programs that require more than 64K bytes of memory for execution. When a task is completed the memory is automatically released for other use.

### SOFTWARE

The S+ operating system, UniFLEX<sup>®</sup> is a multiuser, multitasking operating system based on the UNIX<sup>®</sup> operating system that has been used for many years on Digital Equipment Corp. PDP-11 series minicomputers. It is considered one of the most sophisticated and "user friendly" operating systems available. Variations of UNIX<sup>®</sup> are rapidly becoming standard on mini and larger microcomputers.

A large variety of languages are available for use with the system. These include FORTRAN, COBOL, BASIC, and Pascal. Word processing packages are also available to give you full text processing capability on the system.

Applications programs are available in large quantities in many fields. This includes general business, medical, dental, veterinary, library and real estate management; plus others. Since the system is multiuser it can also be connected to cash registers to produce a point-of-sale terminal system combined with the computer. The possibilities for application of this system are endless.

### THE I/O SYSTEM

The S+ system is totally interrupt driven. All terminal and printer I/O devices connect to an I/O bus separate from the main bus. Up to thirty-two separate devices may be connected to the I/O bus at any one time. If I/O activity is great enough to cause an unacceptable slowdown in system operation, a separate I/O processor can be installed in the system. This plug-in option removes all I/O handling

overhead from the main processor and allows operation of up to thirty-two external devices at 9,600 baud. Without an integrated total design, as in the S+ system, it would become impractical to use a UNIX<sup>®</sup> type operating system in a situation with heavy terminal I/O activity.

### DISK STORAGE

A wide range of disk storage capacity is available for the S+ system, from 2.5 M-byte floppy disks to an 80 M-byte Winchester and many sizes between. All disk controllers use direct memory access (DMA) type operations to maximize data transfer and to minimize overhead on the main processor. The Winchester disks also use intelligent controllers along with DMA transfers to preserve the performance that these type devices are capable of giving. Without this distributed intelligence the system performance would be greatly degraded. The UniFLEX<sup>®</sup> operating system is designed to work at maximum efficiency with this type disk system. The data transfer rates achieved by this combination rival those of large minicomputers.

### COMMUNICATIONS

A high speed local network communications system is available to interconnect S+ systems. The VIA-BUS<sup>®</sup> network will allow communication between systems at data rates of over 400K baud. Such a system makes it possible to share data between local systems in an efficient and low-cost manner.

### AVAILABLE SOON

Tape backup—20M-Byte in less than 15 minutes on a standard ½ inch cartridge.

Mini-Wini—5 and 10 M-Byte Winchesters—5¼ inch package. Winchester performance, for smaller systems in a small package. UniFLEX<sup>®</sup> compatible design.

Large Capacity—190 and 340 M-Byte Winchesters, plus SMD cartridge drives.

*UniFLEX is a registered trademark of Technical Systems Consultants, Inc.*

*UNIX is a registered trademark of Bell Labs.*

*VIA-BUS is a registered trademark of Southwest Technical Products Corporation.*



**SOUTHWEST TECHNICAL PRODUCTS CORPORATION**  
219 W. RHAPSODY  
SAN ANTONIO, TEXAS 78216 (512) 344-0241

```

ldd B,x MATCH
addd #x LINE address
lfr d,u v points to LINE char
ldd B,x increment MATCH
addd #1
add B,x
ldu 4,x v points to STR char
ldd [4,x] NCM
cax #0 >255 ?
bne match3 if so, abort

* Compare LINE and STR
match2 lde #ut STR char
cax #ut same as LINE char?
bne match1 if not, shift LINE
decB end of STR?
bne match2 if not, keep going
bre match4 yes, return

* No match, MATCH=0
match3 ldd #0
std B,x MATCH

* Clean up the stack & return
match4 puls d,v,r,p,c

* * * ICOMP * * *
*
* Integer function ICOMP(STR1,IP0S1,STR2,IP0S2,NCM)
*
global _icomp
text
_icomp leax 2,x x points to argument list
pshs d,v,u

* Find STR2 position
ldd 4,x STR2 address
addd (6,x) IP0S2
subd #1
lfr d,u v-res points to STR2 character

* Find STR1 position
ldd #x STR1 address
addd [2,x] IP0S1
subd #1
lfr d,u v-res points to STR1 character

* Compare characters
ldx [B,x] x is now the character counter
icomp1 leax -1,x decrement counter
cax #0 set N flag
bmi icomp3 identical strings?
lde #ut
cax #ut
bne icomp1 next character

* Characters are different
bhi icomp2 are they in order?
ldd #1 was
bre icomp4
icomp2 ldd $9ffff minus 1
bre icomp4

* Identical strings
icomp3 ldd #0

* Leave result in the stack & return
icomp4 std B,x
puls d,v,u,r,p,c
* * * STOA * * *
*
* subroutine STOA(LINE,ARRAY,LSIZE)
*
global _stoa
text
_stoa leax 2,x x points to argument list
pshs a,v,u

* Set pointers
ldv #x LINE address
ldu [2,x] ARRAY address

* Move characters
ldx (4,x) x is now the byte counter
stos1 leax -1,x decrement counter
cax #0 set N flag
bmi stoa2 done yet?
lde #ut
ste #ut
bre stoa1 next character

* Clean up the stack & return
stoa2 puls a,v,u,r,p,c

* * * ATOB * * *
*
* subroutine ATOB(LINE,ARRAY,LSIZE)
*
global _atob
text
_atob leax 2,x x points to argument list
pshs a,v,u

```

```

* Set pointers
ldv #x LINE address
ldu [2,x] ARRAY address

* Move characters
ldx (4,x) x is now the byte counter
stos1 leax -1,x decrement counter
cax #0 set N flag
bmi stoa2 done yet?
lde #ut
ste #ut
bre stoa1 next character

* Clean up the stack & return
stoa2 puls a,v,u,r,p,c

* * * CHR * * *
*
* subroutine CHR(CHAR,INT)
*
global _chr
text
_chr leax 2,x x points to the argument list
pshs d
ldd [2,x] set each value INT
stb [x] save CHAR
puls d,r,p,c return

end
++

* LISTING 2: FORTRAN source code
*
* * * CONCAT * * *
*
* Concatenate str1 and str2 to form str3
*
subroutine concat(str1,i1,str2,i2,str3,i3)
external naca,cmove,cfill
integer naca
character*1 str1,str2,str3
len1=naca(str1,i1)
len2=naca(str2,i2)
if(len1.gt.13) len1=13
call cmove(str1,i1,str3,i1,len1)
ipos3=len1+1
if(len1+len2.gt.13) len2=13-len1
call cmove(str2,i2,str3,ipos3,len2)
nch=13-len1-len2
ipos3=len1+len2+1
call cfill(' ',str3,ipos3,nch)
return
end

* * * LEFT * * *
*
* Let STR2 = leftmost NCM characters of STR1
*
subroutine left(str1,str2,lsize,nch)
external cmove,cfill
character*1 str1,str2
nchch
if(n.st.lsize) n=lsize
call cmove(str1,i1,str2,i1,n)
lsize=n
n=n+1
call cfill(' ',str2,n,i)
return
end

* * * MID * * *
*
* Move NCM characters from STR1 to STR2 beginning at
* position IP0S1 of STR1.
*
subroutine mid(str1,ipos1,lsize1,str2,lsize2,nch)
nchch
if(ipos1+n.st.lsize1) n=lsize1+1-ipos1
if(n.st.lsize2) n=lsize2
call cmove(str1,ipos1,str2,i1,n)
lsize2=n
n=n+1
call cfill(' ',str2,n,i)
return
end

* * * NUMGET * * *
*
* Extract a decimal integer from LINE
*
integer function numest(line,istr,nch)
character*1 line,kh
external cmove
j=0
k=nch
numest=0
1 k=k-1
if(k.lt.0) go to 2
call cmove(line,istr,kh,i1)
istr=istr+1
if(j.eq.0 .and. (kh.eq.' ' .or. kh.eq.'.')) go to 1
j=1
i=ichar(kh)-48
if(i.lt.0 .or. i.gt.9) go to 2
numest=10*numest+i
go to 1
2 return
end
++

```

## INTRODUCING THE MC68000 EDUCATIONAL COMPUTER

BY

Mary Gallagher  
MOTOROLA, INCORPORATED  
SEMICONDUCTOR PRODUCT SECTOR

As the MC68000 microprocessor has become more and more of an industry standard for 16-bit processors, there has been a corresponding growth in customer requests for low cost educational tools based on the MC68000. The MC68000 Educational Computer Board (MEX68KECB) is Motorola's response to these requests. The MEX68KECB is a single-board MC68000-based computer designed to acquaint professionals and advanced university students with the intricacies of MC68000-based systems. The Educational Computer aids the professional in becoming familiar with the characteristics and capabilities of the powerful MC68000, from its instruction set and programming to system integration.

A significant factor in the customer requests was a requirement for low cost. With this in mind, the Educational Computer was designed to sell for no more than \$500.

In addition to the board, a dumb RS232C-compatible terminal and a power supply (5V @ 1A,  $\pm$  12V @ 50mA) are required.

The Educational Computer Board's hardware/firmware complement offers a lot of capability and a host of functional options. At its heart is a 4MHz version of the MC68000 MPU. Supporting the MPU is a block of two 64K ROMs/EPROMs that provide 16K bytes of firmware. Appropriately called TUTOR, this firmware includes many capabilities designed specifically for educational purposes - a monitor, a debug tool, and an assembler/disassembler.

The monitor/debug portion of TUTOR evolved from MACSbug<sup>TM</sup> and VERSAbug<sup>TM</sup>, the monitors developed for the M68000 EXORmacs<sup>TM</sup> and VERSAmodule<sup>TM</sup> systems. Hence, TUTOR supports all of the MACSbug/VERSAbug commands except those deliberately eliminated because of the hardware limitations of the board (disk I/O for example). But, the

command format and syntax are identical to MACSbug/VERSAbug to permit easy upgrading to these systems, if desired later.

The assembler/disassembler portion of TUTOR is not based on either MACSbug or VERSAbug; it is a truly unique feature of TUTOR which allows a user to begin writing programs immediately in assembly language, avoiding the frustration of hand assembly.

This assembler uses the same command line format and syntax as the MC68000 resident and cross assemblers but differs from these other assemblers in several ways. This assembler is a one-line assembler; that is, each source line is immediately assembled into object code and saved on a line by line basis as it is entered. The source lines are not saved. In order to display a program, the object code must first be translated back into source lines using the disassembler portion of TUTOR.

The MEX68KECB assembler recognizes all MC68000 instruction and addressing mnemonics; labels, line numbers, and comments are not allowed. One assembler directive is also included to provide a way of entering data.

In addition to the on-board firmware, the Educational Board contains 32K of dynamic RAM. The array is organized as a 16K x 16-bit memory block and can be addressed in 16-bit words or in 8-bit bytes.

Two serial RS232C-compatible I/O ports are available on the MEX68KECB. One of the serial ports is used to interface the MEX68KECB to a terminal, required to provide communication between the user and the MC68000. It can range from a high speed CRT terminal to a low speed Teletype. All this is required of the terminal is an RS232C interface. A baud rate generator on the board provides eight clock signals between 110 and 9600 baud. The baud rate of both serial ports is jumper selectable across this range to accommodate varying terminal speeds.



The second serial port is typically used as the interface between the computer board and a host computer. The host can provide mass storage of user generated software as well as more complete software generation capabilities using a resident or cross assembler. The MEX68KECB firmware includes upload and download commands for transferring records between the MEX68KECB and the host. Motorola's EXORcisor and EXORmacs are two potential hosts.

While the serial ports are implemented using two Asynchronous Communications Interface Adapter chips from the M6800 family (MC6850s), parallel I/O and a 24-bit programmable timer are provided by a new Parallel Interface/Timer chip (MC68230) from the 16-bit M68000 family of peripherals. The Parallel Interface and Timer (PI/T) has 24 I/O lines which can function in a variety of I/O configurations.

One parallel I/O port provides a buffered Centronics-compatible printer interface. Hard-copy listings or a complete record of all communications with the terminal can be obtained using this port. Two lines of a second parallel port are designated as an audio tape interface. An audio tape can be used for storage of software instead of, or in addition to, storage by the host computer. The tape format is FSK (frequency shift keying) at a data rate between 1000 and 2000 baud depending on the particular bits sent. Square waves at 2000 and 1000 Hz are used to represent logic ones and zeros. The 24-bit timer generates and measures the period of the square waves. Before being put on tape, information is converted to S-record format, which is a standard data format that includes some error checking.

The MC68230 need not be used exclusively for the printer and tape interfaces. It can be programmed by the user for other I/O and timer functions. Although the MEX68KECB has no expansion bus for connection to other peripheral boards, a small wirewrap area and access to most MC68000 signals as well as to other control signals are provided.

Available at a cost of about \$500, the MC68000 Educational Computer Board is unquestionably the biggest basic computer bargain on the market, particularly when considering the capabilities of the MC68000 microprocessor it employs.

# BIT BUCKET

## ACORN<sup>TM</sup>

COMPUTER SYSTEMS 88-50C



November 28, 1982

'68' MICRO JOURNAL  
3900 Cassandra Smith  
Hixson, TN 37343

Dear Mr. Williams,

This letter is in response to M. E. Villers' letter appearing in the December issue of 68-MICRO. He is just the type of customer we are looking for!

In January 1982, we decided to support the 88-50 computer with a variety of custom quality boards. In our opinion, the 6809 and 88-50 bus is the best combination for computer experimentation. The 88-50 has some of the finest software and the most reliable hardware on the market.

We purchased a Color Computer with the intention of developing a PROM which would convert it to a monitor for the 88-50 computer. When we studied the circuit, we realized that Tandy Radio Shack had copied it chip for chip from the Motorola handbook. The Color Computer has been described as a 500 hp engine with a 2 qt gas tank. There are four systems on the market with a modified "FLEX" to run on the Color Computer. With the modified FLEX, you have a 500 hp engine with a 2 qt gas tank filled with high octane gasoline.

At ACORN COMPUTER SYSTEMS, we are working on the "RS MISSING LINK"(tm), which will allow a 32K Color Computer to become a part of the 88-50 bus. Now, not only will you have FLEX, but all those expansion boards you read about in 68-MICRO. Our system requires no modification or loss of R.S. warranty and repair service. This method may cost a couple hundred dollars more to begin with, but will be cheaper in the long run. You can add 900k of RAM or PROM with extended addressing, an 80 x 24 video board (keeping the CC video for graphics), a calendar

clock, extra ports, a full featured keyboard, voice generator, home control, hard disks, or anything that your heart desires and your pocketbook can afford.

The nice part is that it can be done over a period of years, a little bit at a time. My computer has grown and changed constantly over the past six years. The 88-50 bus, with its many manufacturers, has allowed this personalized expansion.

What we are trying to do, is promote the 88-50 bus through the Color computer. This will bring even more software and hardware to the 68XX system. We hope to have the "RS MISSING LINK"(tm) and our stackable computer modules on the market by March 1983. Write for more information.

Sincerely yours,

*Myke Giesfeldt*  
Myke Giesfeldt, Pres.  
A.C.S. Inc.  
11931 W. Blueground Road  
Milwaukee, WI 53226



PHONE (414) 287-0300

27 August 1983

## MEMORY MAPPING PROBLEMS

Dear Don,

If no one else has, could I bring a small but tricky problem concerning S-BUG to the attention of all 6809 programmers?

During a routine memory check I noticed that I appeared to have 16 more bytes of memory than I paid for! i.e. I could quite happily read and write the first 16 bytes after the MEMEND address! Rather than being plessed with my bonus I proceeded to check the mother board for address aliasing problems (shorted tracks etc.) After a bit of experimenting I discovered that the fault vanished if I removed my DMAF 2 disk controller board. During the testing of said board I suddenly realized that I was not being very bright. A quick check with the help of Brian Roberts of Middlesex Polytechnic (by the way did you know that they run a multi-machine 6809 cluster system for real time control based on SMT machines called POLYFLEX?) confirmed my diagnosis.

When first switched on, SBUG proceeds to locate and remap memory to the correct logical addresses. No matter where your memory is placed, SBUG changes its address to give one 8K block at C000 and the rest as one continuous block from 0000 to MEMEND. What happens if you don't have enough memory to fill all of the available space? What do you find in the 'empty' memory locations? In the old days you'd find 'FF' indicating no memory. However SBUG thinks so much of itself that it fills all the empty memory space with copies of itself! In other words any empty memory blocks are filled with physical address F000-F7FF. If you don't believe me or just want to check, examine X800 and F800, where X is large enough to put you outside your available memory.

This would seem to cause no problem, after all who cares about another copy of SBUG appearing somewhere that you're not going to use. The danger comes from the fact that the DMAF controller board has a physical start address of F000. This means that its 16 DMA address registers appear as the first 16 addresses of every empty memory block! Hence my 'free memory'.

Now just think what a machine code program with a small error that caused it to write to a few bytes just beyond MEMEND could do to your disks!

This problem would of course arise with any peripheral controller card addressed in the F000 region.

Best wishes

*Mike James*

Mike James  
Oak Tree Cottage  
Woodhall, Ankridge  
Leyburn  
N Yorkshire  
England

December 3, 1982

68 Micro Journal  
5900 Cassandra Smith  
Box 849  
Hixson, TN 37343

Dear Don:

Poor Motorola seems to be taking a lot of criticism for the MRDY defect in the 6809. In fairness, we should remember that the defect was well documented in the recent data sheets, including identification of the afflicted parts.

We recently discovered that certain AMI brand 6809 chips exhibit a similar problem. I cannot identify the faulty chip sets, but anyone having an AMI 6809 and using MRDY should probably buy a replacement chip, or contact AMI for identification of the bad parts. It is my understanding current AMI parts no longer exhibit the problem.

We are experiencing no difficulties with current Motorola or Hitachi brand 6809 chips.

Sincerely,  
*Michael Hirsch*  
Michael Hirsch  
Manager, Electronic Systems

ENERGY MANAGEMENT CORP. • 18208 S. VERNON AVE. • GARDENA, CA 90248 • 213/515-0218

Major George B. STRICKOTH  
1611 Marlow Avenue  
Yuma, Arizona 85264  
3 December 1982

Don,

I found Ron Anderson's November column exceptionally enlightening, and of particular interest since I was awaiting the revised edition of Alford & Associates' Screditor III. As Ron had mentioned in his column, contact with ASA revealed that the update/bug cycle was extending beyond their expectations. I must admit that I was getting frustrated, but my particular frustrations were compounded by an uncooperative UPS. When ASA sent the updated program, it was only to be lost by UPS.

'88' Micro Journal

I'd like to report that on 23 November I called ASA with the news that the Screditor III had still not arrived, and they sent one by Federal Express mail, my receiving it on 24 November. I'd also like to very happily report that it's up and running in my SVTPC /09, surpassing my expectations. I am using an Anderson Jacobson 801 K&S daisy wheel printer, and had no problem taking advantage of the proportional spacing, centering, underlining, and bold face capabilities by use of the printer control character function. After five years with the TSC Editor, it sure is nice to see what I'll have as a final product, and to do true cursor editing.

Keep up the great job with the Journal, and thanks for presenting products like the Screditor III in the advertising.

*G.B. Strickoth*  
G.B. STRICKOTH

 **technical systems  
consultants, inc.**

111 Providence Road • Chapel Hill, North Carolina 27514 • (919) 493-1451 • TWX 510-920-0540

Technical Systems Consultants is proud to announce the release of version 1.01 of the UNIFLEX™ COBOL compiler. This new version has all known errors corrected along with the following major product enhancements:

The COBOL compiler has been restructured to enable it to process considerably larger COBOL source programs. The new compiler should handle programs two or three times as large as the previous version.

INDEXED-SEQUENTIAL files which have alternate keys will now run much faster than before. This was accomplished by a minor change in the internal structure of these files. Because of this structural change, all existing INDEXED files must be copied using the "vsamcopy" utility before they can be used with any programs compiled with the new compiler.

The CALL PROGRAM ... USING verb has been changed so that when the called program terminates, control is returned to the calling COBOL program.

The ACCEPT processing has been completely updated. The new ACCEPT verb will allow the user to enter fully edited numeric fields without the necessity of leading zeroes. Also, the processing of alphabetic and alphanumeric fields has been improved.

The new version of the COBOL compiler is now available using the normal product update procedure.

## OMEGASOFT

6809 SYSTEM SOFTWARE

5787 BRANDYWINE CT., CAMARILLO, CA 93010 USA TELEPHONE: 805-987-6426

November 11, 1982

Don Williams  
'68' Micro Journal  
5900 Cassandra Smith Road  
Hixson, TN 37343

Dear Don,

I would like to let everyone know that OmegaSoft has moved into its new quarters at:

5787 Brandywine Ct.  
Camarillo, CA 93010  
Phone (805) 987-6426

I would also like to request that any of OmegaSoft's customers that have a compiler version earlier than 2.1s contact me for upgrade information.

Sincerely,

*Robert Reimiller*  
Robert Reimiller  
Owner, OmegaSoft



WINDRUSH MICROSYSTEMS LTD. (Reg. Office)  
North Walsham, Norfolk NR25 9EA  
Tel: 06921 408188  
Telex: 87380 SHARIT G

'68' MICRO JOURNAL  
New Products Editor  
P.O. Box 849  
Nixon  
Tennessee  
37343 U.S.A.

Your Ref -

Our Ref: WCB/ed 044

Date 15/12/82

#### PRESS RELEASE INFORMATION

Windrush Micro Systems Limited announce the immediate availability of their UNIVERSAL 32K MEMORY BOARD for 88-50C bus 6800 and 6809 computer systems. The product, which retails for £25.00 ex-man (incl VAT), has the following features:

- STATIC MEMORY ensures maximum immunity to data errors, does not require halt, wait, or cycle stealing techniques to refresh memory devices.
- Guaranteed 2MHz operation (tested at 2.2MHz).
- Extended addressing complies with 88-50C specifications.
- RAM, EPROM, ROM, or battery backed CMOS RAM devices may be used in any combination.
- Individual device selects enable each 2K device on the board to be removed from the memory map.
- Generous heat sinking ensures cool operation when configured with 32K of EPROM.
- On-board power supply monitor disables battery backed CMOS RAM devices when the PSU voltage falls below a pre-set limit thus protecting the memory from spurious write's as the system shuts down.
- All data and address lines are fully buffered with the latest generation of shielded input bus receivers to ensure maximum immunity to bus noise and low data error rates.
- Fully socketed.
- Gold plated bus connectors ensure long-term reliability.

B/W Photograph enclosed.

On order - 1/0 Only 10 C Dots more IV 31

Registered in England and Wales No. 1611025 VAT Reg. No. 372 9531 33

**ENDICOTT SOFTWARE**  
P. O. Box 12543  
Huntsville, Alabama 35892  
(205) 881-0506

This joystick has been assembled from proven components to provide trouble-free operation.

**NOTE:** Because of the "tightness" of the new components, the joystick may feel "sticky" in its operation at first. This will disappear after a break-in period, after which you will experience a smooth-acting joystick.

#### WARRANTY

If any part of the joystick malfunctions or breaks during normal usage within 90 days of receipt of the joystick, return the joystick prepaid to Endicott Software and we will repair or replace it (at our option) free of charge. Joysticks subjected to obvious physical abuse are excluded from this warranty.

#### PRESS RELEASE

FROM  
Donald J. Sommer  
3931 South Burns St.  
Seattle, Washington  
98118

This is the first announcement for four Network Design and Analysis programs written in Microsoft BASIC for use on the TRS Color Computer. Most of the programs will play on a 16K machine with extended BASIC. They are available for \$25.00 each on cassette or 5 1/4 inch diskette. If more than one program is ordered on the same cassette or diskette they are \$20.00 for each program.

Program 1 "SHACTIVE" -16k- printer optional  
Design Butterworth, Chebyshev, or Bessel Low-Pass, High-Pass, or Band-Pass Active Filters. The input is in the form of break frequency, band width and filter order.

Program 2 "ACTIVE" -20k- printer optional  
The design portion is the same as "SHACTIVE". A number of special features have been added to permit screen plotting and outputting frequency response data to the printer.

Program 3 "FILTER" -16k- printer optional  
Design Butterworth or Chebyshev Low-Pass, High-Pass, or Band-Pass Passive Electrical Filters. The input is in the form of attenuation, break frequencies and input and output impedances.

Program 4 "LADDER" -16k- printer optional  
The program calculates the frequency response of general networks which are in the ladder topology. This form includes most passive filters. Some latitude is taken in the definition of ladder because of the large number of different elements permitted. This program is an excellent addition to be used with "FILTER" in the design and analysis of passive R-L-C electrical filters.

Don Williams  
68 Micro Journal  
5900 Cassandra Smith Road  
Nixon, TN 37343

December 2, 1982

I am pleased to announce The Mid-America Color Computer Bulletin Board System, known as "MACC-NET".

Located in Kansas City, Missouri, the system is run on a 64K Color Computer, using 3 Shugart disk-drives and an autoanswer modem. The system is up 24 hours a day, except during times of system maintenance and upgrades.

The purpose of this BBS is to provide a forum for Color Computer users to exchange ideas, hints and tricks, problem reports, notes concerning available software and hardware, and as a general sounding board for any aspect of the Radio Shack Color Computer, the Tandy 700-100, and future Color Computer "look-alikes".

A program up-load and down-load feature is available. The program down-load currently has a varied selection of programs. Plans are to include machine-language routines in the down-load, and also text files of additional system documentation, such as ROM addresses, lower memory data areas, etc.

Users of the system can leave messages for each other, leave notices of equipment for sale or wanted to buy. Requests of other users for information, and any information people may have that would be of benefit to others, is always welcome.

An up-load feature has been included, which can be used to send any kind of ASCII data to the system. This can include BASIC programs, Assembler source code, test files of ROM addresses, etc. All data sent to the system will be reviewed before being included on the down-load directory to eliminate duplications. Similar types of informational text may be combined into existing files where appropriate.

The MACC-NET is just beginning operation, and will hopefully grow into a system of immediate use to both beginners and the experienced user. Any comments on system operation and features are welcome.

The MACC-NET telephone number is (816) 358-MACC (6222). The System Operator is Steve Odneal. Voice number is (816) 356-2345 evenings and weekends, usually until 10pm each night.

Steve Odneal  
6606 East 73 Terrace  
Kansas City, Missouri 64113

December 15, 1982

Computer Publishing Center  
'68' Micro Journal  
5900 Cassandra Smith  
PO Box 849  
Nixon, TN 37343

Gentlemen!

The first 68000 based machines are beginning to appear. Recently, we took delivery on an Altos ACS68000 system with a 40mb disk able to handle sixteen users running Bell Lab's Unix Operating System. The cost was less than \$1000/user.

One of the first things we did was to run some benchmarks. Of particular interest was the sieve program from Vol. IV, No. VII, July, 1982. (Also see No. IX.) The 68000 ran this program in 6.3 sec., compared to 22 sec. for Intel C on the 6809 and 15.5 sec. for Whitesmith C on the Z80. However, this is slow compared to the 2 sec. it takes to run on a \$30,000 Zilos System 8000!

There are some offsets. As a practical matter, most programs tend to be I/O bound, and here we find the Altos to have disks that are 2.5 times as fast as the Zilos. In particular, to do 1000 sequential writes followed by 1000 sequential reads and 1000 random reads of 512 byte records takes the Altos only 21.5 sec. The Zilos takes 52.4 sec. This performance results mostly from the use of eight inch Quantar drives as opposed to the much slower 5.25 inch drives others are using. The message is clear. With Unix, the disks are more important than the chip.

Returning to CPU bound processes, the particular implementation of the language and operating system can be important. Zilos has had Unix long enough to have optimized the C compiler for their hardware. Altos is still using the standard version. Zilos keeps as much of the stack as possible in registers. This is not standard practice, and in fact, can hinder pointer manipulations. On the other hand, it can make the system very fast. Finally, the 68000 is presently too fast for the available memory management and RAM chips, so wait states are used on the Altos, further slowing things down. When Altos sets their compiler optimized, obtains faster chips and moves to the 12.5 Mhz CPU, their CPU bound processes should be competitive with anyone's. In the meantime, the overall performance of this system seems to match that of machines costing five times as much.

Rumor has it that a seller machine, which will handle about five users and compete in the \$5-10,000 market is on the way next Spring. An 8086 model has already been announced, so the 68000 model should not be far behind. This machine should be very attractive for the seller business or the larger home.

Sincerely,

*Robert S. Peirce*

Robert S. Peirce  
123 W. Edgewood Dr.  
MC Murray, PA 15317

SPECTRUM PROJECTS  
93-15 84 DRIVE  
WOODHAVEN, N.Y. 11421

Voice line (212) 441-2807

Data lines (212) 441-3755/3766

How To Upgrade A TDP100 To 64K  
By BOB ROSEN of SPECTRUM PROJECTS

The TDP100 has a different RF shield cover from the Color Computer. It only covers the 6803 and the 6 memory chips. You have to go underneath the board and straighten out 7 lugs that are bent parallel to the board. You can then remove the RF shield. There are 3 jumpers to move from 16K to 64K. Two of them are to the left of U21 and the other is above U28. Also you have to add a jumper that is to the left of U17. Then remove capacitors C58, C60, C62, C64, C66, C68, C70 and C72. That's it! There are no jumpers to hook up and pins to bend up to exceed the full 64K. Modifications in this new board (only on TDP100 computer) helps eliminate those extra steps. Some other observations about this new board. It uses a new chip for the keyboard PIA. The 6822 was added to eliminate hash during interrupts. The chips have different numbers than found in the Color Computer Technical Manual. The two PIA's are now U17 and U18 instead of U4 and U8. There is also a better relay for the cassette than was used previously. My final comment is that this board is easier for upgrades and better designed than the series "D" and "E" boards.

109 Carolyn Avenue  
December 3, 1982

Mr. Don Williams  
'68' Micro Journal  
P.O. Box 849  
Mixon, TN, 37343

Dear Mr. Williams:

I need some help. I need routines to initialize my MP-82 at 8000 and list a program to my Microline 82A printer, also I would like to run a program to the printer using TSC cassette basic.

My system consists of the following:

1. SWTPC, 69/A with 96K ram, MP-82 serial interface with A port at 8000 and B Port (control) at 8004, JPC TC-3 interface at 8040 with Motor control and CPU/3 at 8080 in 8 prom running most of the time at

4800 BAUD with several programs at 9600 BAUD (without the TC-3 I would have given up this SWTPC system soon after I bought it).  
2. Fisher CR-110 stereo cassette deck.  
3. Metronics professional keyboard, video display board, and modulator.  
4. RCA television.  
5. Okidata Microline 82A printer.

I have tried everything I can think of to make this printer work with the MP-82. I even sent the MP-82 board back to the factory and asked for some help with routines to make it work, they did not acknowledge receiving the request. They said there was nothing wrong with the interface. I also sent the Microline 82A back to the factory which was returned in eight days. I figured it would be some time before receiving anything back from SWTPC. Being in a hurry to try the printer, I bought a Commodore Computer. There was no problem getting the printer to work with the Commodore, and I was pleased.

The MP-82 came back one month after I sent it in. I have been unable to make the two work together. I even wrote TSC for help, they sent driver routines to be used with Flex, that is all.

I have the printer now running on 8 bit word, 2 stop bits, and no parity. This is the way I would like to use it with the MP-82 and TSC basics.

I would appreciate help in this matter and would be glad to pay for it. I also have a local friend looking for help. He has a SWTPC 6800 unit and wants an 8K basic by SWTPC on Kansas City Standard tape. He sent a check to SWTPC but they returned his check saying the tape basic is no longer available. Where may we find this basic and what is the price?

It appears to me that some companies are so called upgrading themselves right out of business. Most of us have to start with basic units at reasonable price and expand from there. We need lots of support from manufacturers. If we cannot get help from the manufacturer we will go elsewhere. A company will cease to exist, let alone grow without public consent.

Thank you Don.

Sincerely,

*Devery H. Noble, Jr.*  
Devery H. Noble, Jr.  
109 Carolyn Avenue  
Salisbury, Maryland 21801  
(301) 742-6702

THE 6809 COMPANION

Dear Don,

I am writing to tell you of my new book "THE 6809 COMPANION". Although there are one or two books around that deal with the 6809 now, when I started work on the project, six months ago, there were none! I have always been a keen 68 user and was one of the first to have an SWTPC 6800 system in the UK back in the good old days when computers were real computers (i.e. mostly electronics and MIKBUG!). Along with others I slowly worked my way up the tree from cassettes and 4K BASIC to Disks, FLEX and a CIMIX box. When the 6809 arrived I was pleased to discover that I had not been wrong sticking with the S50 Bus. And the use of an S100 machine at work also helped to convince me that I was right!

After writing a few articles for some of the UK computer magazines on the S50 and the 6809 I decided to write a short book on explaining some of the software features of the 6809. After spending so long converting many of my own 6800 programs to the 6809 you can understand why this was the first chapter I started work on. Motorola may say that conversion is easy but there are many things that can go wrong that Motorola never thought of. Believe it or not, this is the chapter I personally most use!

The rest of the book deals with the sorts of topics you'd expect in a machine companion: history, architecture, addressing modes and the instruction set (fully commented). A chapter on programming style tries to point out the strengths of the 6809 and how to make use of them. Interrupts are also dealt with from the point of view of the programmer in yet another chapter.

Although I set out to write a book for programmers, I felt that a chapter on hardware was necessary. All too often have I come across 6809 users (especially in industry) under the impression that hardware comes only from Motorola! So I have tried to give an impression of the S50 bus and what's available, including system software. (I have to admit to being a FLEX freak!)

To cap it all the whole book was produced using an SWTPC 6809, TSC's text editor and typeset using a 6809 program I produced specially.

After all this I wanted to find a publisher who could market my book for a reasonable price and, having prepared my photo-ready copy, my search led me to Babani Publishing. The result is a compact volume costing only £1.95 (approximately \$4.00!) which is cheap enough to buy a new copy when the old one falls apart from over use.

*Mike James*

Best wishes

Mike James  
Oak Tree Cottage, Woodhall  
Askrigg, Leyburn  
N Yorkshire, England.



# PRESS RELEASE COMMUNIQUE DE PRESSE PRESSEINFORMATIONEN

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FOR IMMEDIATE RELEASE

## DoI BACKS COMPUTER SYSTEM FOR DOCTORS

The Department of Industry is to finance up to four of the new ARIES 20/256 GP computer systems for General Medical Practitioners under its "Support for Innovation" scheme.

It will be the first DoI sponsored system to be installed in a GP's surgery as well as the first multi-user GP system to be backed by DoI.

The ARIES 20/256 uses the latest SWTP 3 Plus computer, running the UNIX-like operating system with 256K RAM. Backing storage includes a 20 megabyte 5.25 inch Winchester disk, a 20 Mb tape cartridge archive unit and a 1.3 Mb 8 inch floppy disk drive. Two intelligent VDUs and a 240 cps printer are being provided with each system. The PC can take up to 12 terminals.

The ARIES GP software has been specifically designed to computerize administration in the GP's surgery. It includes programs for GP/sex registers, preventive medicine, patient recall, repeat prescriptions, drug use analysis, patient history taking (MCHS) and word processing.

The first ARIES 20/256 system is to be installed at the Chiswick Health Centre, London W6, just 75 yards from the offices of ARIES Information. These Health Centres are a five-doctor practice with over 13,000 patients.

The GP's participating will also be involved in the valuation program for the "micro for GP's scheme", also backed by the DoI.

The four systems will cost the DoI £90,000.

## For Further Information Contact:

Tim Osmond, ARIES Information Ltd.  
30 Harley Way, Pinner, London, W5. Tel:- 01-894 6477  
Nick van Baten, Intermark PR Ltd. Tel:- 01-637 8752/3  
John Burnett, SWTP Tel:- 0733 524233  
Mike Hildcock, DoI Press Office. Tel:- 01-212 0484

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I ENJOY READING THE JOURNAL AND I HOPE YOU CONTINUE TO BE VERY SELE TIVE IN THE ADS WHICH YOU WILL ACCEPT.

I ONLY HOPE THAT THE MANUFACTURERS OF 8000 EQUIPMENT REALIZE THE IMPORTANCE OF STANDARDIZATION OF BUS AND OPERATING SYSTEM SO THAT WE AS CONSUMERS ARE NO LONGER A SPLINTER GROUP. THE DE FACTO STANDARD OF THE FUTURE WILL BE THE VME BUS AND A UNIX OPERATING SYSTEM. THIS WILL ALLOW 8000 USERS TO UPGRADE FROM 24 BIT ADDRESSING TO 32 BIT ADDRESSING WHEN MOTOROLA ANNOUNCES THE ENHANCEMENT. IT IS MY FEELING THAT MORE PROGRAMS AND BOARDS WILL BE AVAILABLE TO THE USER IF THEY ADHERE TO THE VME BUS AND UNIX. THE 8000 IS THE MICRO OF THE FUTURE. THE SYSTEMS USING THE 8000 CAN BE SIMPLE OR COMPLEX AS THE USER DESIRES. THE COST OF THE 8000 CHIP ITSELF HAS DROPPED CONSIDERABLY IN THE PAST YEAR. THERE ARE A NUMBER OF SECOND SOURCES FOR THE CHIP AND SEVERAL OF THESE SECOND SOURCES ARE PRESENTLY DEVELOPING PERIPHERAL CHIPS WITH MOTOROLA'S BLESSING. THE TECHNOLOGY WILL BE SHARED WITH THE OTHER SECOND SOURCE PARTIES. THE COSTS WILL BE DROPPING ALSO.

THE VME BUS WAS A CONCERTED EFFORT BETWEEN MOTOROLA AND THESE SECOND SOURCES WHICH INCLUDE SOME FROM OVERSEAS. MOTOROLA WILL SUPPORT BOTH THE VERSABUS AND THE VME BUS. THE VME BUS USES A PIN AND SOCKET ARRANGEMENT TO CONNECT BOARDS TO THE MOTHER BOARD. NOT COMPATIBLE WITH THE 5550 OR 5564 BUT IT IS FAMILIAR. THE CARD SIZES ARE STANDARD EUROCARD DIMENSIONS. CARD PRICES WILL COME DOWN AS MORE MANUFACTURERS START PRODUCING CARDS THIS YEAR. OUR "OLD" 8800 AND 8809 MICROS DO NOT HAVE TO BECOME OBSOLETE. THEY CAN BE UTILIZED AS PERIPHERAL CONTROLLERS. MULTIBUS 5564 BUS SYSTEMS WILL SLOWLY BECOME UNPOPULAR AND THEREFORE THEIR CARDS WILL BE MORE COSTLY. AT LEAST ONE FAMILIAR MANUFACTURER IS KEEPING A CLOSE EYE ON VME DEVELOPMENTS (SMOKE SIGNAL BROADCASTING). THERE MAY BE OTHERS LOOKING BUT RELUCTANT TO TIP THEIR HAND AT THIS TIME. WE AS CONSUMERS MIGHT CG WELL TO DELAY A BIT IN COMMITTING OURSELVES.

PITY THE POOR \$100 BUS USERS. THEY WILL HAVE TO START ALL OVER IN ROER TO JUMP ON THE 8000 BANDWAGON. THEIR BUS WILL NOT SUPPORT THE HIGH DATA TRANSFER RATES.

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NOTE: All are as published or received by 68 Micro Journal, some have fixes and patches.

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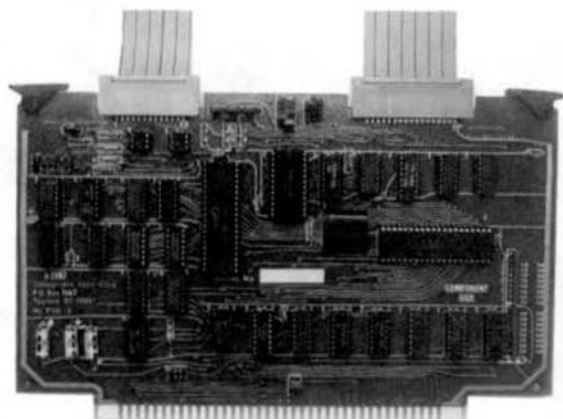
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DETAILED OVERVIEWS OF THE ABOVE PRODUCTS ARE ON PAGES 35/36 OF THE OCTOBER 1982 ISSUE OF '68 MICRO JOURNAL.

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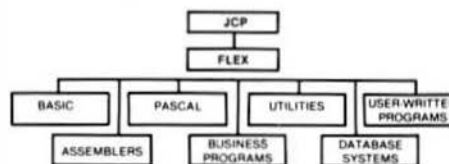
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'68' Micro Journal  
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10. **Where do I order DYNACALC?**  
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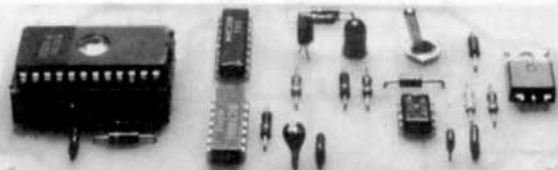
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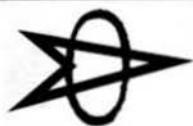
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2758	•	•	•	•	•	•	•
2516	•	•	•	•	•	•	•
2716	•	•	•	•	•	•	•
2716*	•	•	•	•	•	•	•
2 32	•	•	•	•	•	•	•
2732	•	•	•	•	•	•	•
2732A	•	•	•	•	•	•	•
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2764	•	•	•	•	•	•	•
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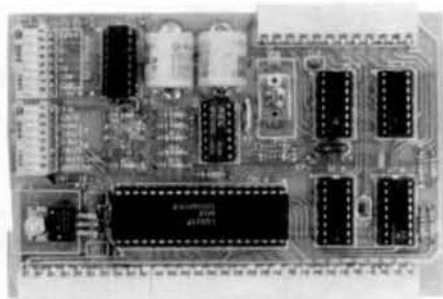
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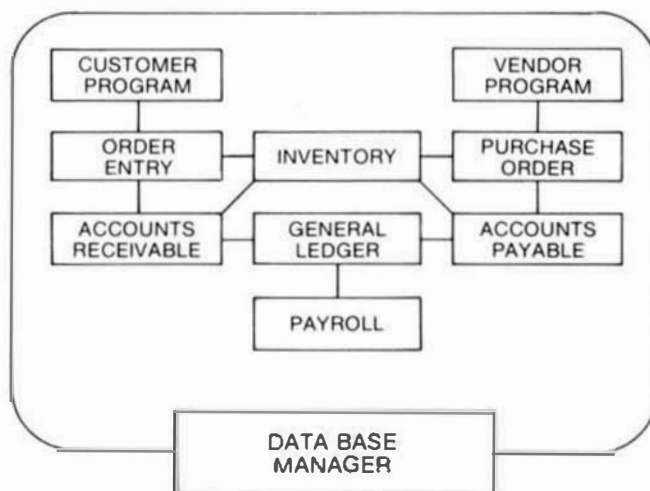
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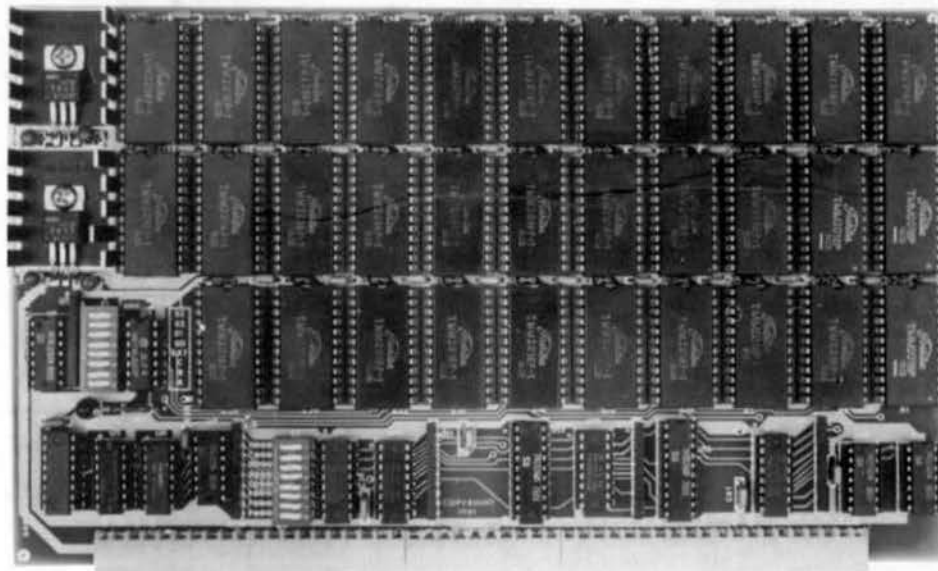
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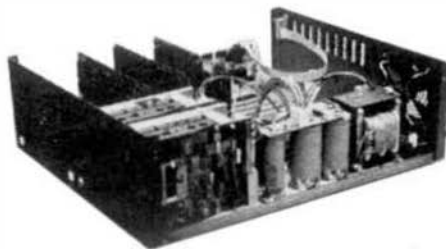
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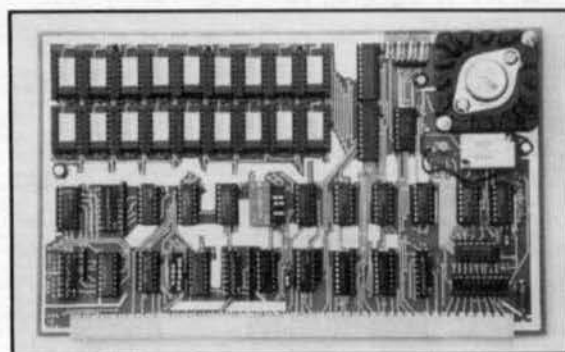
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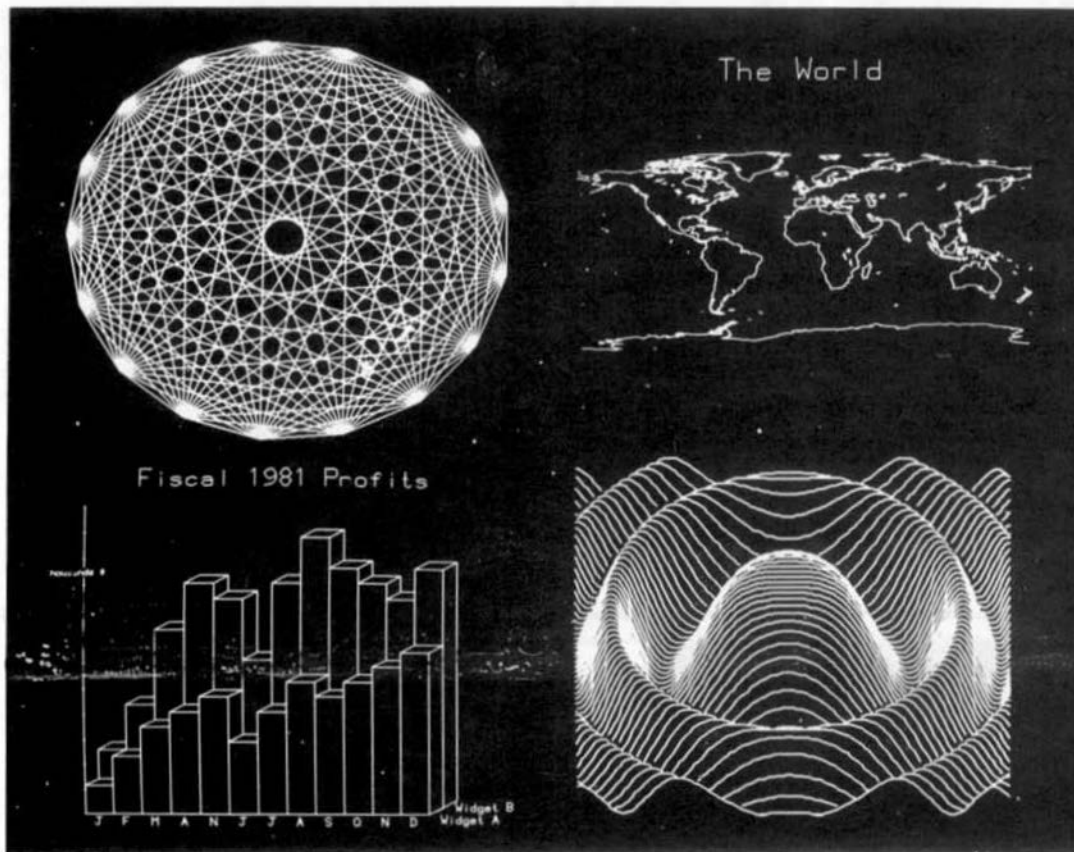
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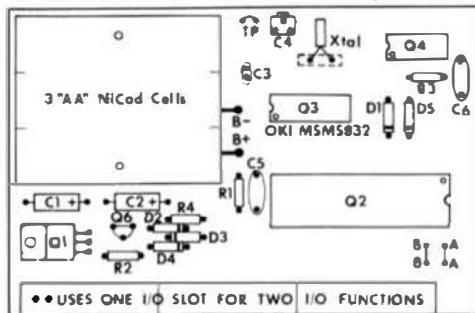
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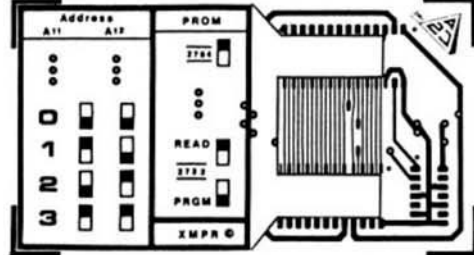
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






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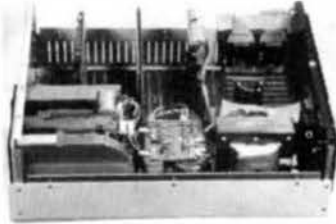
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Company \_\_\_\_\_  
Address \_\_\_\_\_  
City \_\_\_\_\_ State \_\_\_\_\_ Zip \_\_\_\_\_  
Telephone ( \_\_\_\_\_ ) \_\_\_\_\_



# FLEX - OS-9 LEVEL ONE - UNIFLEX - OS-9 LEVEL TWO

## ONLY GIMIX Systems can be configured to run any of these.

GIMIX systems utilize the most powerful 6809 operating systems: FLEX, UniFLEX, OS-9 LEVEL ONE and TWO -- the systems the PROs use. This means a wide selection of software to choose from as well the ability to develop sophisticated, multi-user/multi-tasking programs on your GIMIX System.



**The GIMIX CLASSY CHASSIS™** consists of a heavy-weight aluminum mainframe cabinet which provides more than ample protection for the electronics and 1 or 2 optional 5 1/4" drives.

Backpanel connectors can be added for convenient connection of terminals, printers, drives and other peripherals.

A 3 position locking keyswitch enables users to disable the front panel reset button to prevent accidental or unauthorized tampering with the system.

The GIMIX system mother board provides fifteen 50 pin slots and eight 30 pin I/O slots -- the most room for expansion of any SS50 system available. The on board baud rate generator features 11 standard baud rates, 75 to 38.4K, for maximum versatility and compatibility with other systems. Extended address decoding allows the I/O block to be addressed anywhere in the 1 megabyte address space. All components feature Gold plated connectors for a lifetime of solid connections. All boards are fully buffered for maximum system expansion.

Each GIMIX Mainframe System is equipped with an industrial quality power supply featuring a **ferro-resonant constant voltage transformer** to insure against problems caused by adverse power input conditions such as A.C. line voltage fluctuations etc. The supply provides 8 volts at 30 amps and plus or minus 16 volts at 5 amps, more than enough capacity to power a fully loaded system and two internal drives.

**The 2MHz GIMIX 6809 PLUS CPU board** includes a time of day clock with battery back-up and 6840 programmable timer to provide the programmer with convenient, accurate time reference. Later addition of 9511 or 9512 arithmetic processors is provided for on the board. The unique GIMIX design enables software selection of either OS-9 or FLEX, both included in many complete GIMIX systems.

**GIMIX STATIC RAM boards** require no complicated refresh timing cycles or clocks for data retention. GIMIX memory boards are guaranteed for 2 MHz operation with no wait state or clock stretching required.

Our low power NMOS RAM requires less than 3/4 amp at 8V for a fully populated 64K board. For critical situations, our non-volatile 64K byte CMOS static RAM boards with built in battery back-up retain data even with system power removed. A fully charged battery will power this board for a minimum of 21 days. A write protect switch permits CMOS boards to be used for PROM/ROM emulation and software debugging.

**The GIMIX DMA controller** leaves the processor free to perform other tasks during disk transfers - an important feature for multi-user/multi-tasking systems where processor time allocation is critical. The DMA board will accommodate up to 4 drives 5 1/4" or 8" in any combination running single or double density single or double headed. Programmed I/O Disk Controllers are also available.

**GIMIX systems** are designed with ultimate **RELIABILITY** in mind. You can choose from the below featured systems or select from our wide variety of components to build a custom package to suit your needs.

GIMIX 2MHz 6809 System including: CLASSY CHASSIS, 6809 PLUS CPU BOARD, 56KB STATIC RAM, 2 SERIAL PORTS W/CABLES, GMXBUG MONITOR, FLEX, and OS-9 LEVEL 1 . . . . . **\$3248.49**

FOR TWO 5 1/4" 40 TRACK DSDD DRIVES ADD . . . . . **\$ 900.00**

GIMIX 128KB WINCHESTER SYSTEM including: CLASSY CHASSIS, 6809 PLUS CPU BOARD, 128KB STATIC RAM, 4 SERIAL PORTS W/CABLES, 5 1/4" 80 TRACK DSDD FLOPPY DISK DRIVE, 19MB 5 1/4" WINCHESTER HARD DISK, OS9 LEVEL 2, EDITOR AND ASSEMBLER . . . . . **\$8998.09**

*50HZ Versions Available, 8" Drives Available — Contact GIMIX for Prices and Information.*

### **The Sun Never Sets On A GIMIX!**

GIMIX users are found on every continent, including Antarctica. A representative group of GIMIX users includes: **Government Research and Scientific Organizations** in Australia, Canada, U.K. and in the U.S.; NASA, Oak Ridge, White Plains, Fermilab, Argonne, Scripps, Sloan Kettering, Los Alamos National Labs, AURA, Universites: Carleton, Waterloo, Royal Military College, in Canada; Trier in Germany; and in the U.S.; Stanford, SUNY, Harvard, UCSD, Mississippi, Georgia Tech. Industrial users in Hong Kong, Malaysia, South Africa, Germany, Sweden, and in the U.S.; GTE, Becton Dickinson, American Hoechst, Monsanto, Allied, Honeywell, Perkin Elmer, Johnson Controls, Associated Press, Aydin, Newkirk Electric, Revere Sugar, Hi-G/AMS Controls, Chevron, **Computer mainframe and peripheral manufacturers**, IBM, OKI, Computer Peripherals Inc., Qume, Floating Point Systems, Software houses; Microware, T.S.C., Lucidata, Norpak, Talbot, Stylo Systems, AAA, HHH, Frank Hogg Labs, Epstein Associates, Softwest, Dynasoft, Research Resources U.K., Microworks, Mela Lab, Computerized Business Systems,



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*Quality Electronic products since 1975.*

1982 GIMIX Inc.  
68' Micro Journal



# FLEX & RS COLOR COMPUTER

If you are tired of playing games on your TRS-80C™ Color Computer, or find that you are handicapped by the limitations of the RS BASIC in trying to write a Program that will allow you to actually USE the Color Computer as a COMPUTER, YOU ARE READY TO MOVE UP TO THE FLEX9™ Operating System. If you want to have REAL PROGRAMMING POWER, using an Extremely Powerful Business BASIC, PASCALS, C Compilers, a full-blown Macro Assembler with a Library capability so you are not continuously "reinventing the wheel", YOU ARE READY TO MOVE UP TO THE FLEX9™ Operating System. If you would like to see if YOU REALLY COULD USE A COMPUTER IN YOUR BUSINESS, or begin to make your Computer start PAYING IT'S OWN WAY by doing some Computer Work for the millions of small businesses around you, such as Wordprocessing, Payroll, Accounting, Inventory, etc., then YOU ARE READY TO MOVE UP TO THE FLEX9™ Operating System. How?? DATA-COMP has the way!

DATA-COMP's FLEX9™ Conversion for the TRS-80C™ Color Computer was designed for the SERIOUS COMPUTER USER; with features like greatly increased Display Screens, WITH Lower Case Letters, so you can put a FULL Menu on ONE Screen, or see SEVERAL Paragraphs at the same time; with features like providing a FULL Keyboard so you have FULL Control of your Computer AND it's Programs NATURALLY, without needing a chart to see what Key Combination will give you what function; with USER ORIENTED functions to make using the Operating System natural, like having the Computer AUTOMATICALLY determine what type of Disk is being used in what type of Disk Drive and working accordingly, rather than that you have to specify each and every thing for it, or like having the Computer work with the Printer you have been using all along without you having to tell the new Operating System what is there, etc., etc., etc.

DATA-COMP has everything you need to make your TRS-80C™ Color Computer WORK for YOU; from Parts and Pieces to Full, Ready To Use SYSTEMS. DATA-COMP designs, sells, services, and SUPPORTS Computer SYSTEMS, not just Software. CALL DATA-COMP TODAY to make your Computer WORK FOR YOU!

## System Requirements

FLEX9 Special General Version x Editor & Assembler (which normally sell for \$50.00 ea.)	\$150.00
F-MATE(RS) FLEX9 Conversion Rout. for the RS Disk Controller when purchased with Special General FLEX9 Sys.	\$69.95
when purchased without the General FLEX9 Sys.	\$79.95
Set of Eight 64K RAM Chips w/Mod. Instructions	\$99.95
Color Computer with 64K RAM and EXT. BASIC	\$549.95
Color Computer with 16K RAM	\$375.95
Color Computer with 16K RAM and EXT. BASIC	\$465.95

## Now Available

### Enhanced F-MATE Version 2.1

## SPECIAL SYSTEM PACKAGES

64K Radio Shack COLOR COMPUTER, Radio Shack COLOR DISK CONTROLLER, a Disk Drive System, Special General Version of FLEX9™, F-MATE(RS)™ and a Box of 10 Double Density Diskettes; a COMPLETE, ready to run SYSTEM on your Color TV Set. **\$1249.95**

## DISK DRIVE PACKAGES, etc.

These Packages include the Radio Shack Disk Controller, Disk Drives with Power Supply and Cabinet, and Disk Drive Cable:

PAK #1 — 1 Single Sided, Double Density Sys.	\$499.95
PAK #2 — 2 Single Sided, Double Density Sys.	\$769.95
PAK #3 — 1 Double Sided, Double Density Sys.	\$599.95
PAK #4 — 2 Double Sided, Double Density Sys.	\$949.95

## PARTS AND PIECES

Radio Shack Disk Controller	\$179.95
1 ea. Single Sided, Double Density Disk Drive	\$249.95
1 ea. Double Sided, Double Density Disk Drive	\$349.95
Single Drive Cabinet with Power Supply	\$89.95
Double Drive Cabinet with Power Supply	\$109.95
Single Drive Disk Cable for RS Controller	\$24.95
Double Drive Disk Cable for RS Controller	\$34.95
Micro Tech. Prods., Inc. LOWER CASE ROM Adapter	\$74.95
Radio Shack BASIC Version 1.1 ROM	\$34.95

## SOFTWARE



Requires FLEX™ and one of the following CRT terminals

Now Runs On Any Type Terminal

### Features:

- Two display boards.
- Four levels of play.
- Point scoring system.
- Play white or black.
- Change or set-up board piece positions.
- Forfeit move.
- Swap sides.
- Make move and swap sides.
- Change skill level.
- Stop and restart game.
- Solve Mate in 1-2-3-4 moves.

\$79.95 Specify 5" or 8" disk

This is one of the strongest CHESS programs running on any microcomputer, estimated USCR Rating 1600

Note: Personal checks allow 3-4 week delivery.

## DIET-TRAC Forecaster

A Diet Planning and Analysis Program

DIET-TRAC Forecaster is a program that plans a diet in terms of energy, calories and percentage of carbohydrates, proteins and fats (C.P.F. %) or grams of Carbohydrate, Protein and Fat food exchanges of each of the six basic food groups (vegetable, bread, meat, skim milk, fruit and fat) for a specific individual.

Sex, Age, Height, Present Weight, Frame Size, Activity Level and Basal Metabolic Rate for normal individuals are taken into account. Ideal weight and sustaining calories for any weight of the above individual are calculated. When a weight goal is given (either gain or loss), and a calorie plan is agreed upon between the computer and the individual, the number of days to reach the weight goal is projected. The starting and ending rate of weight loss is calculated, and a daily calendar with each day's predicted weight for a 30-day period is printed.

FLEX VERSION	\$59.95
UNIFLEX VERSION	\$89.95

## PRINTERS

The Epson MX-80

\$495.00

The Epson MX-100

\$725.00

MX-70 \$355.00 MX-80 FT \$575.00

## MEMORY

SWTPC-Motorola, MP32

32K Dynamic Memory Board

Assembled & Tested

1 MHz - No extended addressing

Can be set up for \$0-7FFF or 8000-FFFF

**\$149.95**



**DATA-COMP**  
**SOUTH EAST MEDIA**

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1-615-842-4601

## Verbatim Diskettes.

5" Soft Sector Disks	
Single Side Single Density	\$2.75 ea
Single Side Double Density	\$2.75 ea
Double Side Double Density	\$4.92 ea
Plastic Storage Box	\$7.00 ea

8" Soft Sector Disks	
Single Side Single Density	\$3.75 ea
Single Side Double Density	\$4.10 ea
Double Side Double Density	\$4.75 ea
Plastic Library Box	\$5.00 ea

Foreign Orders Add 10% Surface—20% Air Mail

## WINCHESTER BACKUP UTILITIES

The following utilities allow the backup of any size disk system to any size diskettes.

By simply inserting diskettes as requested by COPY-MULT, a large disk system (Winchester, etc.) may be downloaded to your present floppy disk system, any size. No need to fiddle with directory deletions or any of the other tedious operations that must be done using a normal copy routine.

COPYMULT-CMD understands normal "copy" syntax and always keeps up with files already copied by maintaining directories for both host and receiving disk system, thus eliminating hours of tedious keyboard entries and other time consuming cleanup chores.

BACKUP-CMD is a special program that downloads "random" type files, any size.

RESTORE-CMD a special program to restructure copied "random" files for copying, or recopying back to the host system.

FREELINK-CMD a "bonus" utility that reinks the free chain of a floppy or hard disk thereby eliminating fragmentation.

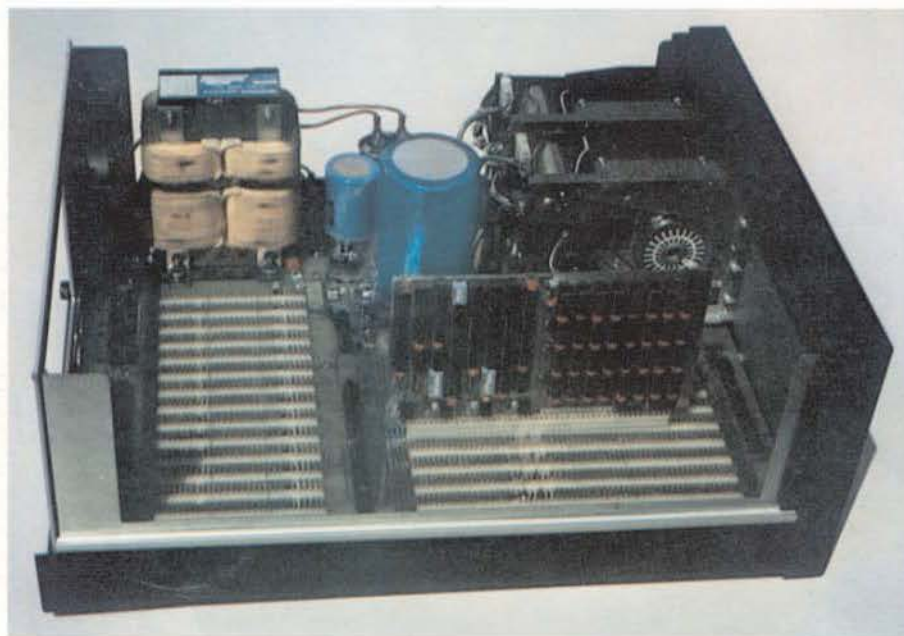
\*\* Completely documented source files included.

\*\* ALL 3 Programs \$99.50 on 8" diskette



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# HELIX™



## THE MAINFRAME

- Industry Standard Optima™ Cabinet
- Largest Constant Voltage Power Supply in the industry
- S-64 Bus gives 16 Bit Power and S-50 Bus Compatibility
- 10 Main (S-64) Slots
- 14 I/O (S-30) Slots plus 2 On-board
- On-board Baud Rate Generator to 38.4Kb
- Space and Power for two 5 1/4" Disk Drives
- Full Address Decoding for I/O Slots
- Two RS-232 Serial and Two parallel Ports On-board
- Single Board Construction for Reliability
- Faraday Shielded Bus Lines give "Text Book Clean" Signals

## THE PROCESSORS

### 6809

- Standard 2 MHz Operation
- Standard DAT Compatible with GIMIX and SWTPC
- Standard 6840 Internal Timer
- Standard 1K Scratchpad RAM
- Standard Clock/Calendar with Battery
- Provision for Programmers Console

### 68000

- Standard 8 MHz Operation
- Memory Management Hardware
- Provision for Programmers Console
- 16 Bit Power and 8 Bit Compatibility



## THE POWER SUPPLY

- Ferro-resonant Transformer for Line Noise and Under-Voltage Protection
- Conservative 25 Amps at 8.5 Volts
- Conservative 5 Amps at  $\pm 16$  Volts
- Conservative Component Rating for Reliability

## THE COMPONENTS

- Fully Socketed
- Gold Plated Bus Connectors
- Only "B" Series 68XX Components Used
- Only Top Grade Logic Circuits Used
- Industrial Grade Components Throughout

The HELIX™ computer system represents the latest advance in S-50 bus computer systems. Relying on the physical nature of S-50 bus connectors to guarantee compatibility, the HELIX adds 14 bus lines (becoming S-64) to allow a 68000 processor to operate with full 16 bit data transfer and 24 bit addressing, while at the same time providing full interchangeability with existing S-50 components.

Offered with a selection of processors, memories, and peripheral controllers, a HELIX system can be configured for applications ranging from advanced hobbyist to multiterminal time-sharing.

Designed to offer the utmost in speed, reliability, and utility at a reasonable price, it represents a new standard of quality for those who require a professionally designed computer for professional use.

## THE MEMORIES

- DM-64
- Field Proven
- Proprietary Memory Control Logic
- Fully Transparent Refresh
- Tested at 2.5 MHz Operation
- DM-512
- 512K Bytes on a Single S-64 Board
- 16 Bit Power and 8 Bit Compatibility
- Runs in Existing S-50 Systems where Physical Space Allows
- Full 24 Bit Addressing
- Fully Transparent Refresh

## THE PRICES

Because of the variety of configurations possible, full pricing cannot be given. Representative prices are:

- 64K 6809 HELIX ..... \$1995
- 64K 68000 HELIX ..... \$2595
- 512K 6809 HELIX ..... \$4450
- 512K 68000 HELIX ..... \$4995

# HAZELWOOD COMPUTER SYSTEMS

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